SUPER®

SUPER® X7SBi-LN4

USER'S MANUAL

Revision 1.0

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Manual Revision 1.0
Release Date: March 18, 2008

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Preface

About This Manual

This manual is written for system integrators , PC technician and knowledgeable PC users. It provides information for the installation and use of the SUPER® X7SBi-LN4 motherboard. The X7SBi-LN4 supports single Intel® Xeon®3000 Sequence or Core™2 Quad and Duo series processor with a system bus speed of 1333/1066/800 MHz. The Intel® Xeon® 3000 Sequence or Core™2 processor supports the 775-Land Grid Array Package that interfaces with the motherboard via an LGA775 socket. With support of Quad-Core/Dual-Core Technology, Wide Dynamic Execution, FSB Dynamic Bus Inversion (DBI), Advanced Digital Media Boost, Smart Memory Access, and Thermal Management 2 (TM2), the X7SBi-LN4 delivers unparalleled system performance and great power efficiency in a slim package. Please refer to the motherboard specifications pages on our web site (http://www.supermicro.com/Products/) for updates on supported processors. This product is intended to be professionally installed.

Manual Organization

Chapter 1 describes the features, specifications and performance of the mainboard and provides detailed information about the chipset.

Chapter 2 provides hardware installation instructions. Read this chapter when installing the processor, memory modules and other hardware components into the system.

Chapter 3 describes troubleshooting procedures for the video, the memory and the system setup stored in CMOS, in case you encounter problems.

Chapter 4 includes an introduction to BIOS and provides detailed information on running the CMOS Setup utility.

Appendix A and **Appendix B** provide BIOS POST Messages and POST Codes. **Appendix C** and **Appendix D** list OS Installation and other Software Programs Installation Instructions.

Conventions Used in the Manual

Special attention should be given to the following symbols for proper installation and to prevent damage done to the components or injury to yourself:

Warning: Important information given to ensure proper system installation, to prevent bodily injury or damage to the components.

Note: Additional Information given to differentiate various models or to ensure correct system setup.

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Chapter 1

Introduction

1-1 Overview

Checklist

Congratulations on purchasing your computer motherboard from an acknowledged leader in the industry. Supermicro boards are designed with the utmost attention to detail to provide you with the highest standards in quality and performance.

Please check that the following items have all been included with your motherboard. If anything listed here is damaged or missing, contact your retailer.

The following items are included in the retail box:

One (1) Supermicro Mainboard

One (1) floppy drive ribbon cable (CBL-0022L)

One (1) IDE cable (CBL-0036L-03)

Six (6) SATA cables (CBL-044L)

One (1) Supermicro CD containing drivers and utilities

One (1) User's/BIOS Manual

One (1) I/O shield (CSE-PT07L)

Contacting Supermicro

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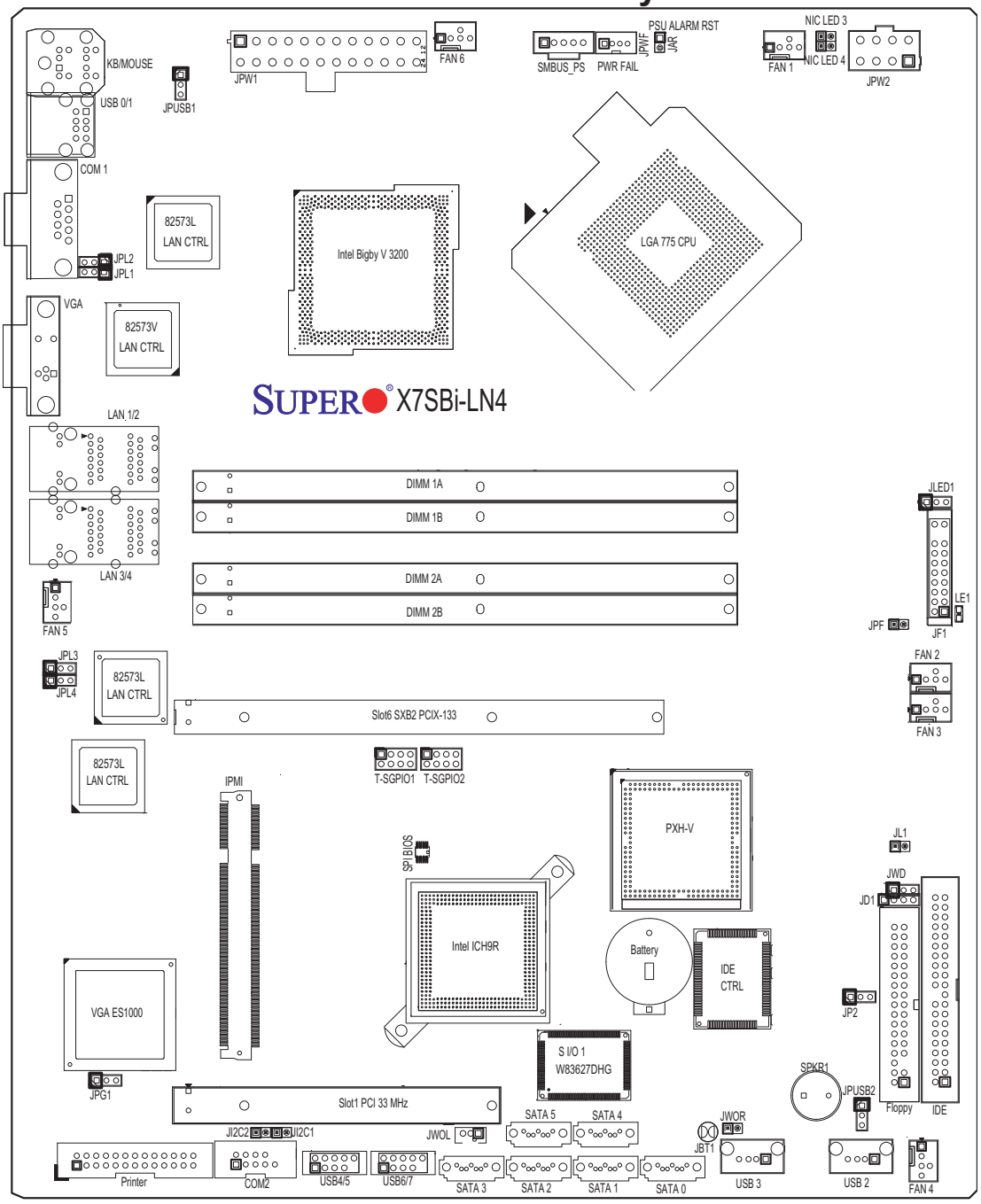
SUPER® X7SBi-LN4 Image

Note: *All pictures and drawings shown in this manual were based upon the latest PCB Revision available at the time of publishing of the manual. The motherboard you have received may or may not look exactly the same as those in this manual.*

Motherboard Layout



Important Notes to the User

- Jumpers not indicated are for testing only.
- See Chapter 2 for detailed information on jumpers, I/O ports and JF1 front panel connections.
- "▪ " indicates the location of Pin 1.
- To configure RAID settings, please refer to RAID Configuration User Guides posted on our web site at www.supermicro.com/support/manuals.
- To repair or replace the onboard SPI BIOS chip, please return the motherboard to RMA Dept. at Supermicro for service.

X7SBi-LN4 Quick Reference

Jumpers	Description	Default Setting
JAR	Power Alarm Reset	Open (Normal)
JBT1	CMOS Clear	See Chapter 2
JI²C1/JI²C2	SMB to PCI Slots	Open/Open (Disabled)
JP2	IDE Enable/Disable	Pins 1-2 (Enabled)
JPF	Power Force On	Open (Disabled)
JPG1	VGA Enable/Disable	Pins 1-2 (Enabled)
JPL1/JPL2/JPL3/JPL4	GLAN 1/2/3/4 Enable	Pins 1-2 (Enabled)
JPUSB1/JPUSB2	USB 0-1/USB 2-7 Enable	JPUSB1:Pins1-2(Ena.),JPUSB2:Pins 2-3(Dis.)
Watch Dog	Watch Dog Timer Enable	Pins 1-2 (Reset)

Connectors	Description
COM1/COM2	COM Port/Serial Port 1-2 Connectors
Fans 1-6	Fan 6: CPU Fan, Fan 1-5: System Fan Headers
Floppy	Floppy Disk Connector
IDE	IDE Hard Drive Connector (Blue)
IPMI	SIMSO IPMI 2.0 Socket
JBT1	CMOS Clear
JD1	External Speaker Header
JF1	FP Control Panel Header
JL1	Chassis Intrusion Header
JLED1	Onboard Power LED Header
JOH	Overheat LED Header
PWR Fail	Power Fail
JPW1	ATX 24-pin Power Connector (Required)
JPW2	12V 8-pin Power Connector (Required)
JWOL	Wake-on-LAN Header
JWOR	Wake-on-Ring Header
KB/Mouse	PS/2 Keyboard/Mouse
LAN1-LAN4	Ethernet RJ45 (Gigabit LAN) Connectors
Printer	Parallel Printer Port
SATA 0-5	SATA Headers
SGPIO 1-2	Serial General Purpose Input Output Ports
Slot 1	PCI 32-bit 33 MHz (3.3V)
Slot 6	SBX2 PCI-X 133 MHz (3.3V)
SMB_PS	Power SMBus (I^2C)
SPKR1	Internal Speaker/Buzzer
USB 0-1	Back Panel Universal Serial Bus Ports
USB 2-3	Front Panel (Onboard) USB Ports
USB 4-7	Front Panel Accessible USB Headers
VGA	Video Graphics Port

LED Indicator	Description
LE1	Standby Power LED Indicator
NIC LED3/NIC LED4	LED Indicators for GLAN Port 3/GLAN Port 4

Motherboard Features

Processor ◆ *Latest CPU technology!*

- Single Intel® Xeon® 3000 Sequence or Core™2 Quad and Duo series processor with a system bus speed of 1333/1066/800 MHz
- Supports Intel Dual Core Technology, Wide Dynamic Execution, FSB Dynamic Bus Inversion (DBI), Advanced Digital Media Boost, Smart Memory Access, and Thermal Management 2 (TM2)

Memory ◆ *Latest memory technology!*

- Four DIMM slots support Dual/Single Channel DDR2 800/667 MHz up to 8 GB of ECC/Non-ECC Unbuffered DDR2 SDRAM

Chipset ◆ *Latest Intel chipset technology!*

- Intel 3200
- Intel ICH9R
- Intel PXH-V

Expansion Slots

- One (1) 32-bit PCI 33MHz (3.3V)
- One (1) PCI-X 64-bit 133 MHz (3.3V)

BIOS

- 8 Mb Firmware Hub Phoenix BIOS (Note 1)
- DMI 2.3, PCI 2.2, ACPI 1.0/2.0, Plug and Play (PnP), SMBIOS 2.3

PC Health Monitoring

- Onboard voltage monitors for CPU cores, Memory Voltage, +1.8V, +3.3V, +5V, +5V Standby, +12V, –12V, and VBAT
- CPU 4-phase-switching voltage regulator
- Status monitor for fan speed & System OH/Fan Fail LED/Control
- Pulse Width Modulation Fan Control & Low noise fan speed control
- Environmental temperature monitoring via BIOS
- Power-up mode control for recovery from AC power loss
- SuperDoctor III, NMI
- System Resource alert via SuperDoctor III

ACPI Features

- Slow blinking LED for suspend state indicator
- Main switch override mechanism
- External modem ring-on

Onboard I/O

- 1 ATA 100 EIDE Channel
- Intel ICH9R SATA Controller, 6 connectors for 6 devices with support of RAID* functions 0, 1, 5 and 10 (in the Windows environment) (Note 2)
- 1 floppy port interface (up to 2.88 MB)
- Fast UART 16550 compatible serial port and 1 header
- Intel 82573V and 82573L Gigabit Ethernet Controllers
- PS/2 mouse and PS/2 keyboard ports
- Up to 8 USB (2 rear ports, 2 headers, and 2 on-board connectors)
- VGA Port
- SIMSO IPMI 2.0 Slot
- Winbond 83627HG (Super I/O), Hardware Monitor: W83793G
- ATI ES1000 w/32MB Video Memory

Temperature

- Monitoring CPU, chassis environment
- CPU Thermal Trip support
- Thermal Monitor 2 (TM2) (available if supported by the CPU)

Other

- Wake-on-LAN (JWOL)
- Wake-on-Ring (JWOR)
- Onboard Standby Power warning LED Indicator ("LE 1")

CD Utilities

- Drivers and software for Intel 3200 chipset utilities

Dimensions

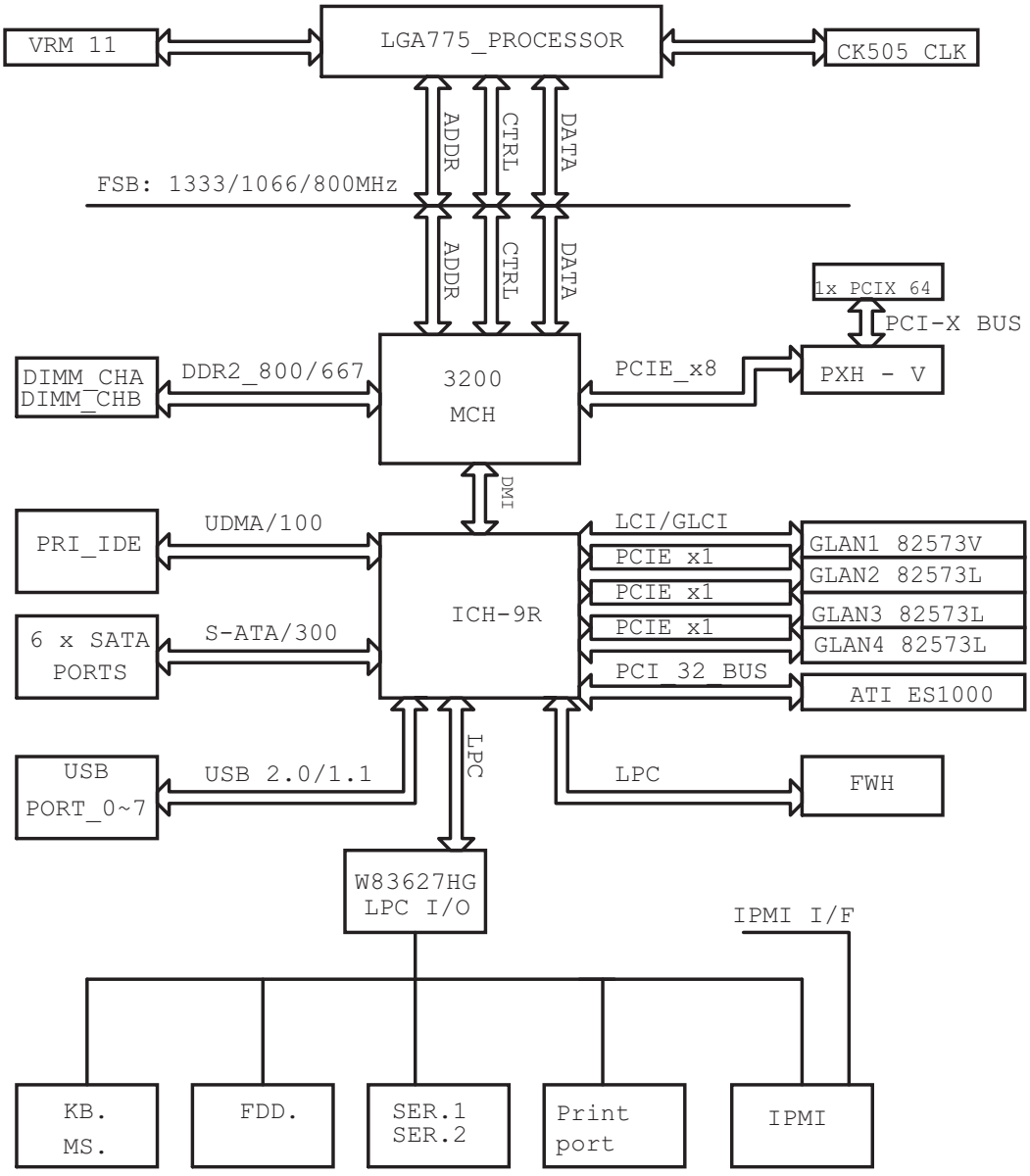
- ATX form factor, 12" x 9.6" (304.8 x 243.8 mm)

Notes:

1. To repair or replace the onboard SPI BIOS chip, please return the motherboard to RMA Dept. at Supermicro for service.

2. To configure RAID settings, please refer to RAID Configuration User Guides posted on our web site at www.supermicro.com/support/manuals.

Block Diagram



X7SBi-LN4
System Block Diagram

Note: *This is a general block diagram and may not exactly represent the features on your motherboard. See the previous pages for the actual specifications of your motherboard.*

1-2 Chipset Overview

The Intel 3200 chipset is comprised of two primary components: the Memory Controller Hub (MCH) and the I/O Controller Hub (ICH9R). The X7SBi-LN4 provides the performance and feature-set required for the mainstream server market.

Memory Controller Hub (MCH)

The function of the MCH is to manage the data flow between four interfaces: the CPU interface, the DDR2 System Memory interface, the PCI Express interface, and the Direct Media Interface (DMI). The MCH is optimized for the Intel® Xeon® 3000 Sequence processor in the LGA775 Land Grid Array package. It supports one or two channels of DDR2 SDRAM.

Intel ICH9R System Features

The I/O Controller (ICH9R) provides the data buffering and interface arbitration required for the system to operate efficiently. It also provides the bandwidth needed for the system to maintain its peak performance. The Direct Media Interface (DMI) provides the connection between the MCH and the ICH9R. The ICH9R supports two PCI-Express devices, six Serial ATA ports, and up to eight USB 2.0 ports/headers. In addition, the ICH9R offers the Intel Matrix Storage Technology which provides various RAID options for data protection and rapid data access. It also supports the next generation of client management through the use of PROActive technology in conjunction with Intel's next generation Gigabit Ethernet controller. Functions and capabilities include:

- Advanced Configuration and Power Interface, Version 2.0 (ACPI)
- Power SMBus (I^2C)
- SST/PECI for Speed Control
- SPI Flash BIOS
- Low Pin Count (LPC) Interface

Note: *For more information on the ICH9R, please refer to Intel's website at www.intel.com.*

1-3 Special Features

Recovery from AC Power Loss

BIOS provides a setting for you to determine how the system will respond when AC power is lost and then restored to the system. You can choose for the system to remain powered off (in which case you must hit the power switch to turn it back on) or for it to automatically return to a power- on state. See the Power Lost Control setting in the Advanced section to change this setting. (Default: Last State).

1-4 PC Health Monitoring

This section describes the PC health monitoring features of the X7SBi-LN4. All have an onboard System Hardware Monitor chip that supports PC health monitoring.

Onboard Voltage Monitoring

An onboard voltage monitor will scan the following voltages continuously:CPU Cores, Memory Voltage, +3.3V, +5V, +5V Standby, +12V, −12V, Memory and Vbatt.(via SuperO Doctor). Once a voltage becomes unstable, a warning is given or an error message is sent to the screen. Users can adjust the voltage thresholds to define the sensitivity of the voltage monitor.

Fan Status Monitor with Firmware Control

The PC health monitor can check the RPM status of the cooling fans. The onboard CPU and chassis fans are controlled by Thermal Management via BIOS (under Hardware Monitoring in the Advanced Setting).

Environmental Temperature Control

The thermal control sensor monitors the CPU temperature in real time and will turn on the thermal control fan whenever the CPU temperature exceeds a user-defined threshold. The overheat circuitry runs independently from the CPU. Once it detects that the CPU temperature is too high, it will automatically turn on the thermal fan control to prevent any overheat damage to the CPU. The onboard chassis thermal circuitry can monitor the overall system temperature and alert users when the chassis temperature is too high.

With the Intel ICH9R built in, the X7SBi-LN4 supports I/O Virtualization Technology (VT-d) that enables multiple operating systems and applications to run in independent partitions. Each partition uses its own subset of host physical memory, and behaves like a virtual machine (VM), providing isolation and protection across

multiple partitions. This feature is available when a processor that supports I/O Virtualization Technology is installed on the motherboard.

CPU Overheat LED and Control

This feature is available when the user enables the CPU overheat warning function in the BIOS. This allows the user to define an overheat temperature. When this temperature is exceeded, both the overheat fan and the warning LED are triggered.

System Resource Alert

This feature is available when used with Supero Doctor III in the Windows OS environment or used with Supero Doctor II in Linux. Supero Doctor is used to notify the user of certain system events. For example, if the system is running low on virtual memory and there is insufficient hard drive space for saving the data, you can be alerted of the potential problem. You can also configure Supero Doctor to provide you with warnings when the system temperature goes beyond a pre-defined range.

1-5 ACPI Features

ACPI stands for Advanced Configuration and Power Interface. The ACPI specification defines a flexible and abstract hardware interface that provides a standard way to integrate power management features throughout a PC system, including its hardware, operating system and application software. This enables the system to automatically turn on and off peripherals such as CD-ROMs, network cards, hard disk drives and printers. This also includes consumer devices connected to the PC such as VCRs, TVs, telephones and stereos.

In addition to enabling operating system-directed power management, ACPI provides a generic system event mechanism for Plug and Play and an operating system-independent interface for configuration control. ACPI leverages the Plug and Play BIOS data structures while providing a processor architecture-independent implementation that is compatible with Windows 2000, Windows XP and Windows 2003 Server Operating Systems.

Slow Blinking LED for Suspend-State Indicator

When the CPU goes into a suspend state, the chassis power LED will start blinking to indicate that the CPU is in suspend mode. When the user presses any key, the CPU will wake-up and the LED will automatically stop blinking and remain on.

1-6 Super I/O

The disk drive adapter functions of the Super I/O chip include a data separator, write pre-compensation circuitry, decode logic, data rate selection, a clock generator, drive interface control logic and interrupt and DMA logic. The Super I/O supports two 360 K, 720 K, 1.2 M, 1.44 M or 2.88 M disk drives and data transfer rates of 250 Kb/s, 500 Kb/s or 1 Mb/s.

It also provides two high-speed, 16550 compatible serial communication ports (UARTs). Each UART includes a 16-byte send/receive FIFO, a programmable baud rate generator, complete modem control capability and a processor interrupt system. Both UARTs provide legacy speed with baud rate of up to 115.2 Kbps as well as an advanced speed with baud rates of 250 K, 500 K, or 1 Mb/s, which support higher speed modems.

The Super I/O provides functions that comply with the ACPI (Advanced Configuration and Power Interface), which includes support of legacy and ACPI power management through a SMI or SCI function pin. It also features auto power management to reduce power consumption.

Chapter 2

Installation

2-1 Static-Sensitive Devices

Electrostatic-Discharge (ESD) can damage electronic components. To prevent damage to your system board, it is important to handle it very carefully. The following measures are generally sufficient to protect your equipment from ESD.

Precautions

- Use a grounded wrist strap designed to prevent static discharge.
- Touch a grounded metal object before removing the board from the antistatic bag.
- Handle the board by its edges only; do not touch its components, peripheral chips, memory modules or gold contacts.
- When handling chips or modules, avoid touching their pins.
- Put the motherboard and peripherals back into their antistatic bags when not in use.
- For grounding purposes, make sure your computer chassis provides excellent conductivity between the power supply, the case, the mounting fasteners and the motherboard.
- Use only the correct type of onboard CMOS battery as specified by the manufacturer. Do not install the onboard battery upside down to avoid possible explosion.

Unpacking

The motherboard is shipped in antistatic packaging to avoid static damage. When unpacking the board, make sure the person handling it is static protected.

2-2 Motherboard Installation

Be sure to mount the motherboard into the chassis before you install the CPU onto the motherboard. All motherboards have standard mounting holes to fit different types of chassis. Make sure that the locations of all the mounting holes for both motherboard and chassis match. Make sure that the metal standoffs click in or are screwed in tightly. Then use a screwdriver to secure the motherboard onto the motherboard tray.

Note: *Some components are very close to the mounting holes. Please take precautionary measures to prevent damage to these components when installing the motherboard to the chassis.*

2-3 Processor and Heatsink Installation

Warning: When handling the processor package, avoid placing direct pressure on the label area of the fan.

Notes:

1. Always connect the power cord last and always remove it before adding, removing or changing any hardware components. Make sure that you install the processor into the CPU LGA 775 socket before you install the CPU heatsink.

2. The Intel LGA 775 Processor package contains the CPU fan and heatsink assembly. If you buy a CPU separately, make sure that you use only Intel-certified multi-directional heatsink and fan.

3. Make sure to install the motherboard into the chassis before you install the CPU heatsink and fan.

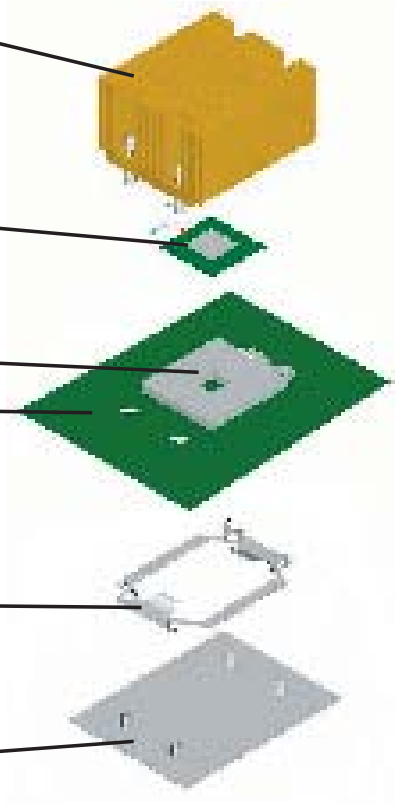
4. When purchasing an LGA 775 Processor or when receiving a motherboard with an LGA 775 Processor pre-installed, make sure that the CPU plastic cap is in place and none of the CPU pins are bent; otherwise, contact the retailer immediately.

5. Refer to the MB Features Section for more details on CPU support.

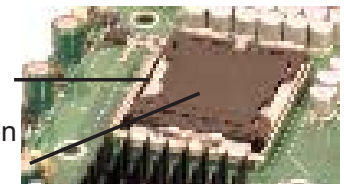
Installation Overview

For proper system setup, please follow the following procedure:

1. Install the heatsink backplate into the chassis if needed.
2. Install the motherboard into the chassis.
3. Install the CPU onto the motherboard.
4. Install the heatsink and/or cooling fans (if any).
5. Connect the fan and power cables (if any).



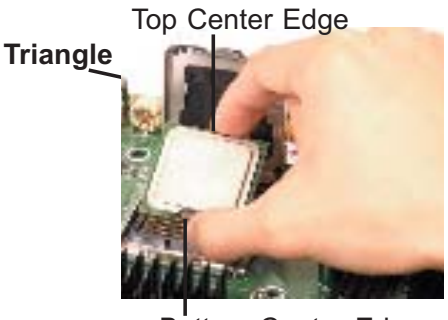
Installing the LGA 775 Processor



1. Press the load lever to release the load plate, which covers the CPU socket, from its locking position.

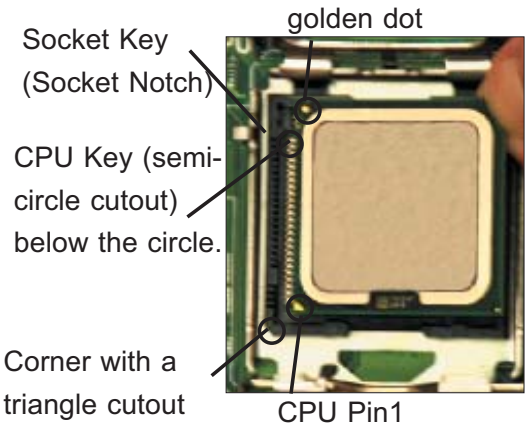
2. Gently lift the load lever to open the load plate.

3. Use your thumb and your index finger to hold the CPU at the top center edge and the bottom center edge of the CPU.



4. Align CPU Pin1 (the CPU corner marked with a triangle) against the socket corner that is marked with a triangle cutout.

5. Align the CPU key that is the semi-circle cutout below a golden dot against the socket key, the Notch on the same side of the triangle cutout on the socket.



6. Once aligned, carefully lower the CPU straight down to the socket. (**Do not drop the CPU on the socket. Do not move the CPU horizontally or vertically. Do not rub the CPU against the surface or against any pins of the socket to avoid damage to the CPU or the socket.)

7. With the CPU inside the socket, inspect the four corners of the CPU to make sure that the CPU is properly installed.

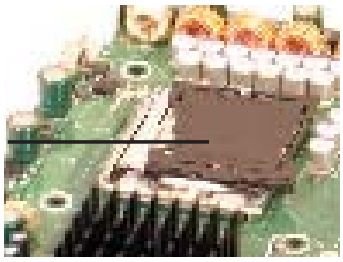
8. Use your thumb to gently push the load lever down to the lever lock.

9. If the CPU is properly installed into the socket, the plastic PnP cap will be automatically released from the load plate when the load lever is pushed in the lever lock. Remove the PnP cap from the motherboard.

Warning: Please save the plastic PnP cap. The motherboard must be shipped with the PnP cap properly installed to protect the CPU socket pins. Shipment without the PnP cap properly installed will cause damage to the socket pins.

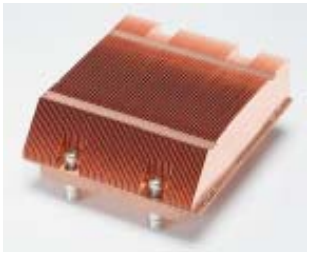
CPU in the CPU socket

Plastic cap is released from the load plate if CPU properly installed.

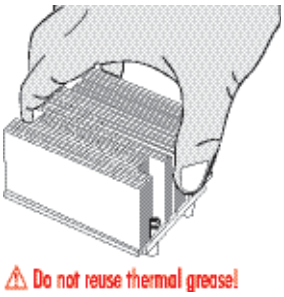


Installing the Heatsink

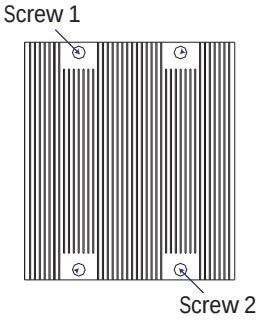
1. Do not apply any thermal grease to the heatsink or the CPU die, the required amount has already been applied.



2. Place the heatsink on top of the CPU so that the four mounting holes are aligned with those on the retention mechanism.



3. Screw in two diagonal screws (eg. the #1 and the #2 screws) until just snug (do not fully tighten the screws, which may damage the CPU).

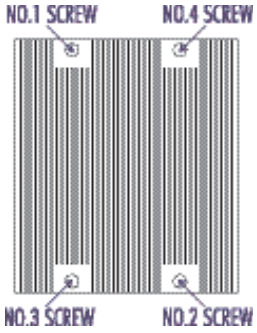


4. Finish the installation by fully tightening all four screws.

Removing the Heatsink

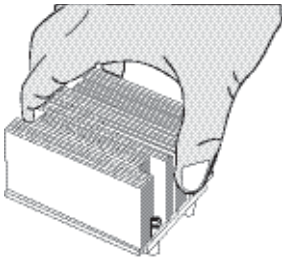
Warning: We do not recommend that the CPU or the heatsink be removed. However, if you do need to un-install the heatsink, please follow these instructions below to prevent damage to the CPU or the CPU socket.

1. Unscrew and remove the heatsink screws from in the sequence as show in the picture on the right.



2. Hold and gently wriggle the heatsink to loosen it from the CPU. (Do not use excessive force when loosening the heatsink!)

3. Once the heatsink is loosened, remove it from the CPU socket.



4. Clean the surfaces of the CPU and the heatsink to remove the old thermal grease. Reapply the proper amount of thermal grease on the surface before re-installing the CPU and the heatsink.

2-4 Installing DIMMs

Note: *Check the Supermicro web site for recommended memory modules.*

Warning: Exercise extreme care when installing or removing DIMM modules to prevent any possible damage. Also note that the memory is interleaved to improve performance (see step 1).

DIMM Installation

1. Insert the desired number of DIMMs into the memory slots, starting with DIMM1A. The memory scheme is interleaved so you must install two modules at a time, beginning with DIMM1A and DIMM2A then DIMM1B and DIMM2B

2. Insert each DIMM module vertically into its slot. Pay attention to the notch along the bottom of the module to prevent inserting the DIMM module incorrectly.

3. Gently press down on the DIMM module until it snaps into place in the slot. Repeat for all modules (see step 1 above).

Memory Support

The X7SBi-LN4 supports up to 8 GB of Unbuffered ECC/Non-ECC DDR2 800/677 MHz in 4 DIMM slots. Populating DIMM1A,DIMM2A, and/or DIMM1B, DIMM2B with memory modules of the same size and of the same type will result in dual channel, two-way interleaved memory which is faster than the single channel, non-interleaved memory.

Notes:

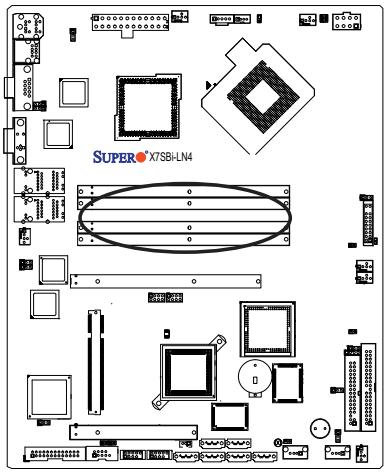
1. Due to the OS limitations, some operating systems may not show more than 4GB of memory.

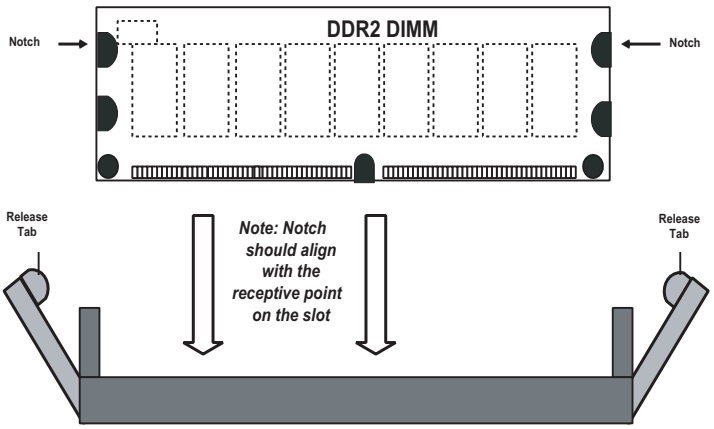
2. Both Unbuffered ECC and Non-ECC memory modules can be installed in the memory slots. When ECC memory is used, it may take 25-40 seconds for the VGA to display.

3. Due to memory allocation to system devices, memory remaining available for operational use will be reduced when 4 GB of RAM is used. The reduction in memory availability is disproportional. (Refer to the following Memory Availability Table for details.)

Possible System Memory Allocation & Availability		
System Device	Size	Physical Memory Remaining (-Available) (4 GB Total System Memory)
Firmware Hub flash memory (System BIOS)	1 MB	3.99
Local APIC	4 KB	3.99
Area Reserved for the chipset	2 MB	3.99
I/O APIC (4 Kbytes)	4 KB	3.99
PCI Enumeration Area 1	256 MB	3.76
PCI Express (256 MB)	256 MB	3.51
PCI Enumeration Area 2 (if needed) -Aligned on 256-MB boundary-	512 MB	3.01
VGA Memory	16 MB	2.85
TSEG	1 MB	2.84
Memory available to OS and other applications		2.84

Installing and Removing DIMMs



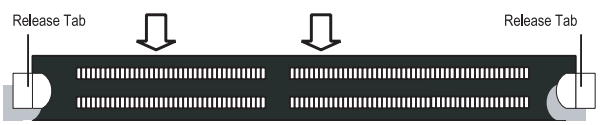


To Install: Insert module vertically and press down until it snaps into place. Pay attention to the alignment notch at the bottom.

To Remove:
Use your thumbs to gently push the release tabs near both ends of the module. This should release it from the slot.

Top View of DDR2

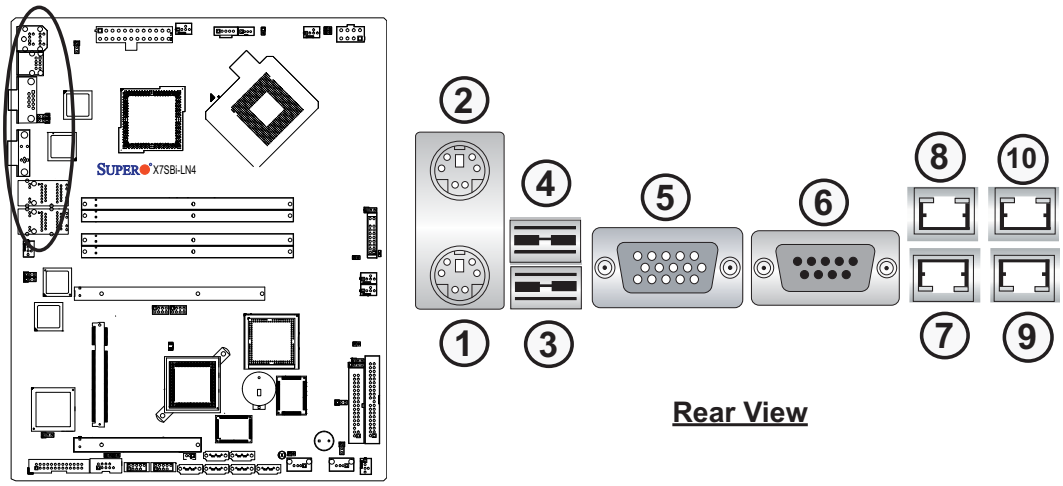
Top View of DDR2 Slot



2-5 Control Panel Connectors/IO Ports

The I/O ports are color coded in conformance with the PC 99 specification. See the image below for the colors and locations of the various I/O ports.

1. Back Panel Connectors/IO Ports



Back Panel I/O Port Locations and Definitions

Back Panel Connectors

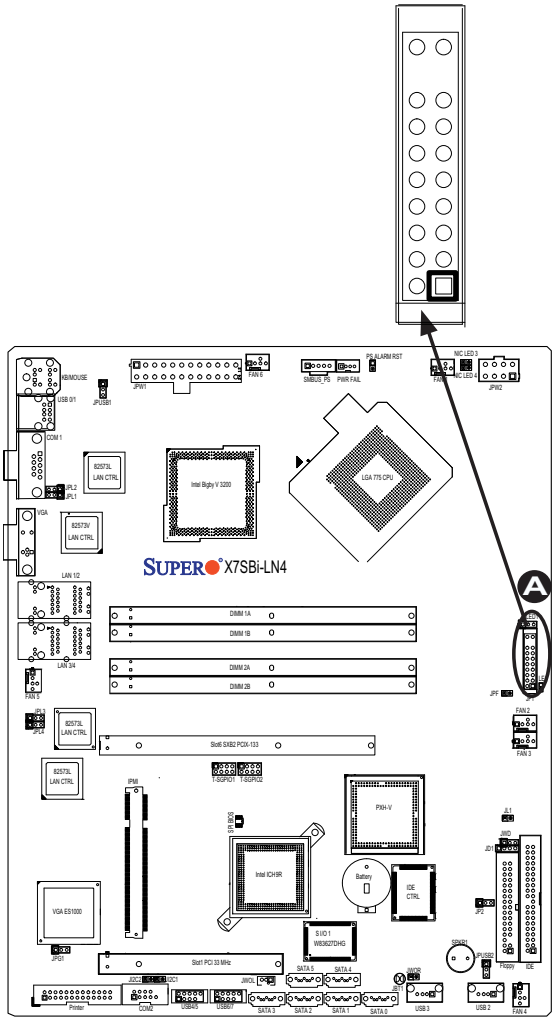
1. Keyboard (Purple)
2. PS/2 Mouse (Green)
3. Back Panel USB Port 1
4. Back Panel USB Port 0
5. COM Port 0 (Turquoise)
6. VGA Port (Blue)
7. Gigabit LAN 2
8. Gigabit LAN 1
9. Gigabit LAN 4
10. Gigabit LAN 3

(*See Section 2-5 for details.)

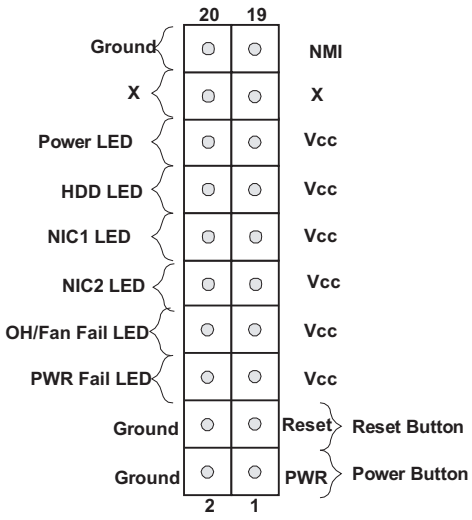
2. Front Control Panel

JF1 contains header pins for various buttons and indicators that are normally located on a control panel at the front of the chassis. These connectors are designed specifically for use with Super Micro server chassis. See the following image for descriptions of the various control panel buttons and LED indicators. Refer to the following section for descriptions and pin definitions.

JF1 Header Pins



A. JF1



3. Front Control Panel Pin Definitions

NMI Button

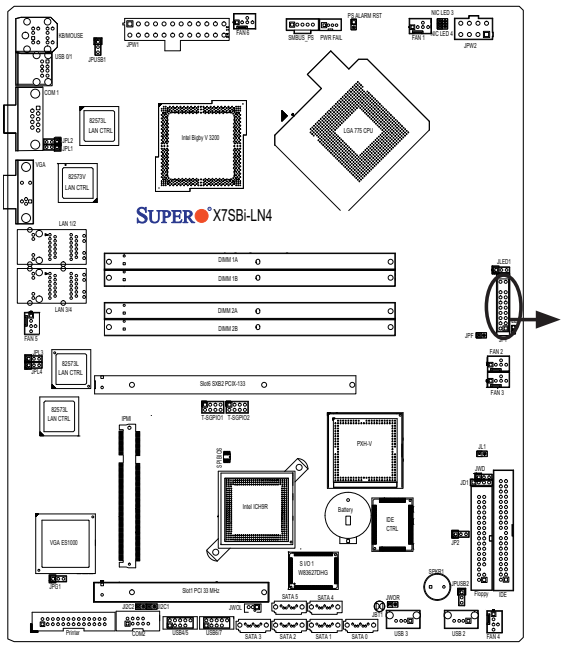
The non-maskable interrupt button header is located on pins 19 and 20 of JF1. Refer to the table on the right for pin definitions.

NMI Button Pin Definitions (JF1)	
Pin#	Definition
19	Control
20	Ground

Power LED

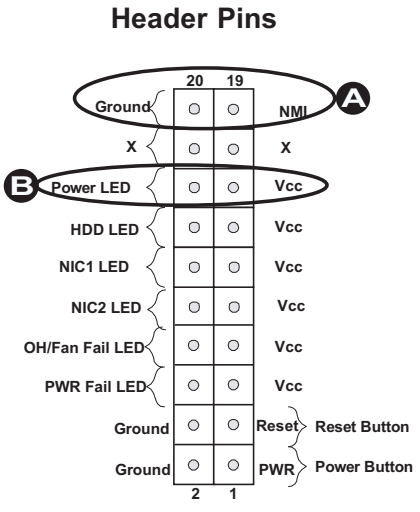
The Power LED connection is located on pins 15 and 16 of JF1. Refer to the table on the right for pin definitions.

Power LED Pin Definitions (JF1)	
Pin#	Definition
15	+5V
16	Ground



A. NMI

B. PWR LED



HDD LED

The HDD LED connection is located on pins 13 and 14 of JF1. Attach the hard drive LED cable here to display disk activity (for any hard drives on the system, including SAS, Serial ATA and IDE, if available). See the table on the right for pin definitions.

HDD LED Pin Definitions (JF1)	
Pin#	Definition
13	+5V
14	HD Active

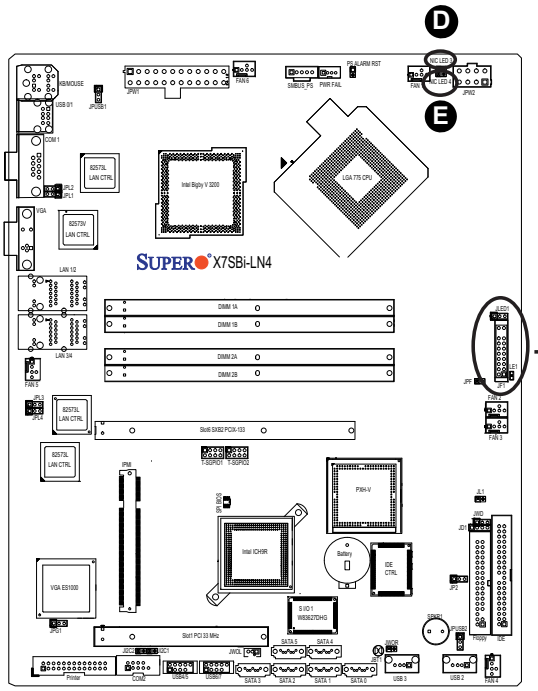
NIC1/NIC2/NIC3/NIC4

LED Indicators

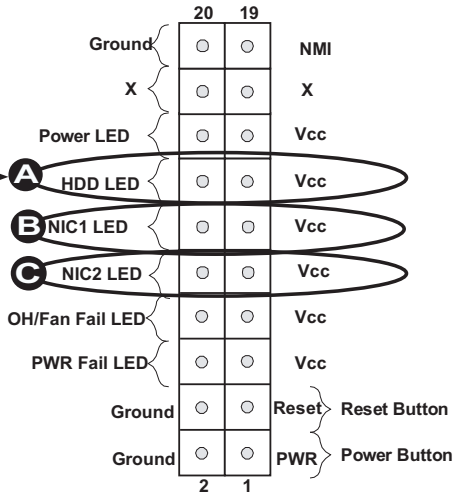
The NIC (Network Interface Controller) LED connection for GLAN port1 is located on pins 11 and 12 of JF1, for GLAN port2 on Pins 9 and 10. The NIC LED connection for GLAN port 3 is located at NIC3 LED, for GLAN port 4 is located at NIC4 LED. Attach the NIC LED cables for display network activity. Attach the NIC LED cables to display network activity. Refer to the table on the right for pin definitions.

GLAN 1/2 LED Pin Definitions (JF1)	
Pin#	Definition
9/11	LED_Anode+
10/12	NIC LED Signal

GLAN 3/4 LED Pin Definitions	
Pin#	Definition
1	LED_Anode+
2	NIC LED Signal



A. HDD LED
B. NIC1 LED
C. NIC2 LED
D. NIC3 LED
E. NIC4 LED



Overheat/Fan Fail LED (OH)

Connect an LED to the OH/Fan Fail connection on pins 7 and 8 of JF1 to provide advanced warning of chassis overheating or fan failure. Refer to the table on the right for pin definitions.

OH/Fan Fail Indicator Status (JF1)	
State	Definition
Off	Normal
On	Overheat
Flash-ing	Fan Fail

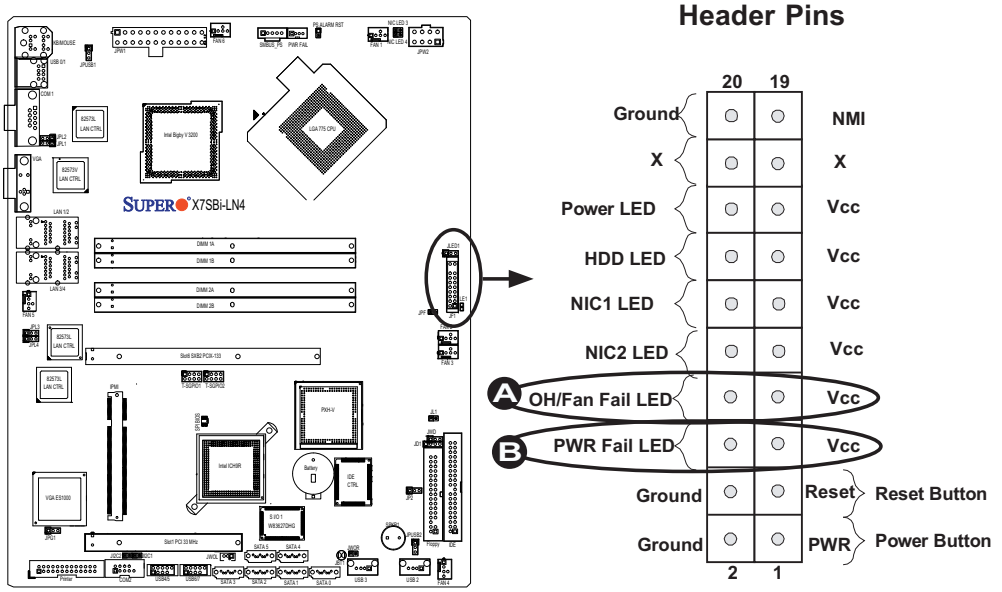
Power Fail LED

The Power Fail LED connection is located on pins 5 and 6 of JF1. Refer to the table on the right for pin definitions.

PWR Fail LED Pin Definitions (JF1)	
Pin#	Definition
5	Vcc
6	Ground

A. OH/Fan Fail LED

B. PWR Supply Fail



Reset Button

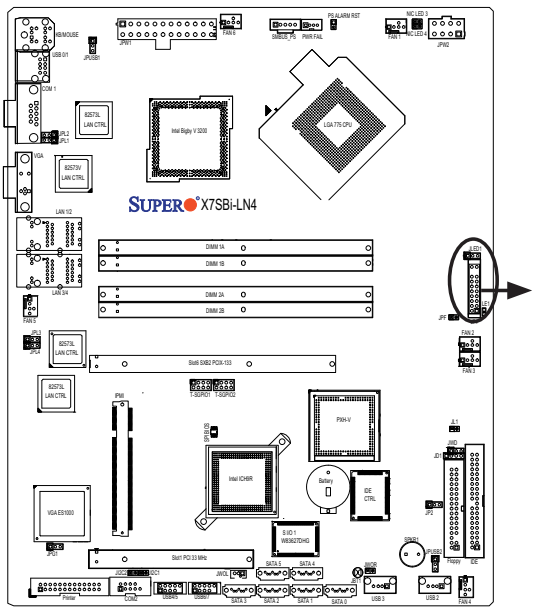
The Reset Button connection is located on pins 3 and 4 of JF1. Attach it to the hardware reset switch on the computer case. Refer to the table on the right for pin definitions.

Reset Button Pin Definitions (JF1)	
Pin#	Definition
3	Reset
4	Ground

Power Button

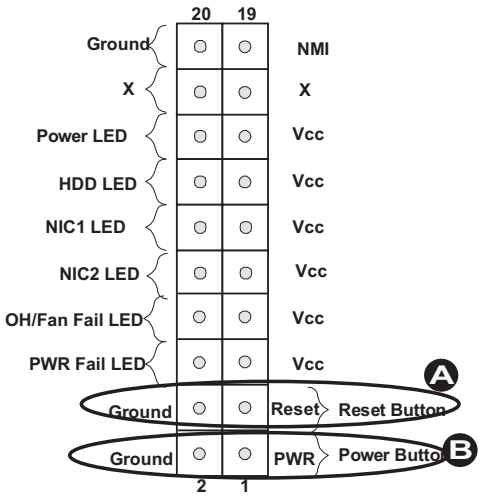
The Power Button connection is located on pins 1 and 2 of JF1. Momentarily contacting both pins will power on/off the system. This button can also be configured to function as a suspend button (with a setting in the BIOS - see Chapter 4). To turn off the power when set to suspend mode, press the button for at least 4 seconds. Refer to the table on the right for pin definitions.

Power Button Pin Definitions (JF1)	
Pin#	Definition
1	Signal
2	+3V Standby



A. Reset Button

B. PWR Button



2-6 Connecting Cables

ATX Power Connector

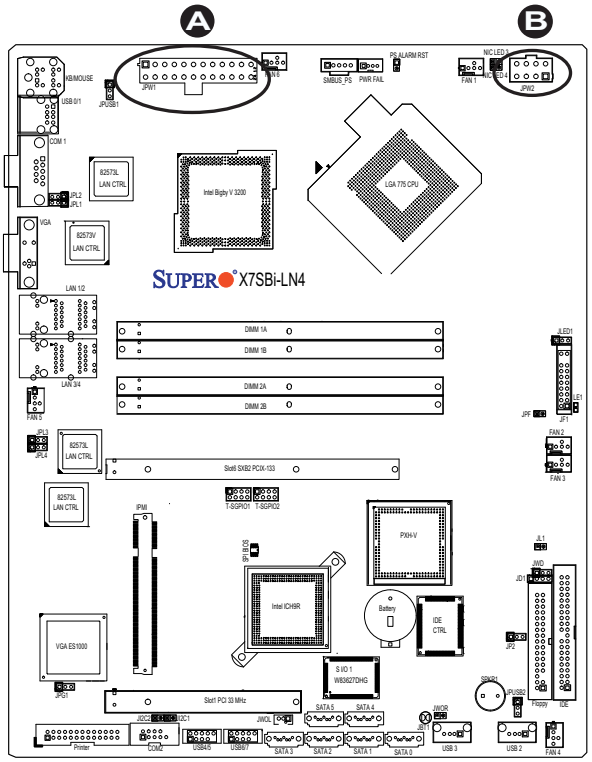
There are a 24-pin main power supply connector(JPW1) and an 8-pin CPU PWR connector (JPW2) on the motherboard. These power connectors meet the SSI EPS 12V specification. For the 8-pin PWR (JPW2), please refer to the item listed below.

ATX Power 24-pin Connector Pin Definitions (JPW1)			
Pin#	Definition	Pin #	Definition
13	+3.3V	1	+3.3V
14	-12V	2	+3.3V
15	COM	3	COM
16	PS_ON	4	+5V
17	COM	5	COM
18	COM	6	+5V
19	COM	7	COM
20	Res (NC)	8	PWR_OK
21	+5V	9	5VSB
22	+5V	10	+12V
23	+5V	11	+12V
24	COM	12	+3.3V

Processor Power Connector

In addition to the Primary ATX power connector (above), the 12V 8-pin CPU PWR connector at JPW2 must also be connected to your power supply. See the table on the right for pin definitions.

12V 8-pin Power CPU Connector Pin Definitions (JPW2)	
Pins	Definition
1 through 4	Ground
5 through 8	+12V



(Required Connection)

A. JPW1

B. JPW2

Universal Serial Bus (USB)

There are eight USB 2.0 (Universal Serial Bus) ports/headers on the motherboard. Two of them are Back Panel USB ports (USB 0/1). Another four USB Headers: USB 4/5 and USB 6/7 can provide front access USB connections. FP USB 2/3 are front panel USB connections. See the tables on the right for pin definitions.

Back Panel USB (0/1) Pin Definitions

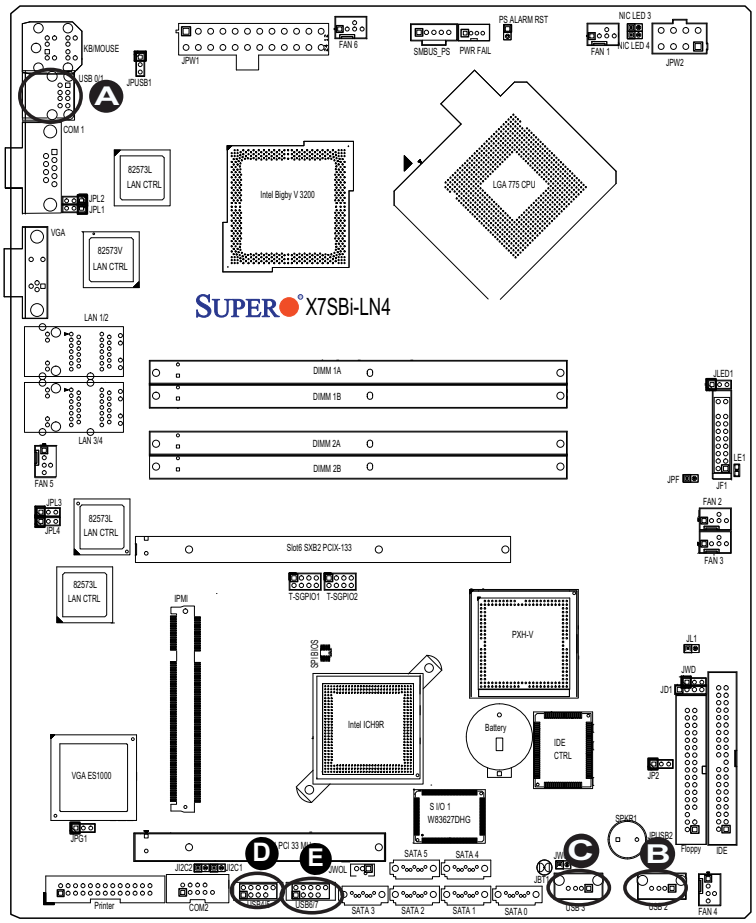
Pin#	Definitions
1	+5V
2	PO-
3	PO+
4	Ground
5	N/A

Front Panel USB (2/3) Pin Definitions

Pin#	Definitions
1	Vcc
2	PO-
3	PO+
4	Ground
5	N/A

Front Accessible USB Pin Definitions

USB4, USB6 Pin #	Definition	USB5, USB7 Pin #	Definition
1	+5V	1	+5V
2	PO-	2	PO-
3	PO+	3	PO+
4	Ground	4	Ground
5	No connection	5	Key



A. Backpanel USB0/1
B. Front Panel USB2
C. Front Panel USB3
D. Front Accessible USB4/5
E. Front Accessible USB6/7

ATX PS/2 Keyboard and PS/2 Mouse Ports

The ATX PS/2 keyboard and the PS/2 mouse are located at J28. See the table on the right for pin definitions. (The mouse port is above the keyboard port. See the table on the right for pin definitions.)

PS/2 Keyboard and Mouse Port Pin Definitions

Pin#	Definition
1	Data
2	NC
3	Ground
4	VCC
5	Clock
6	NC

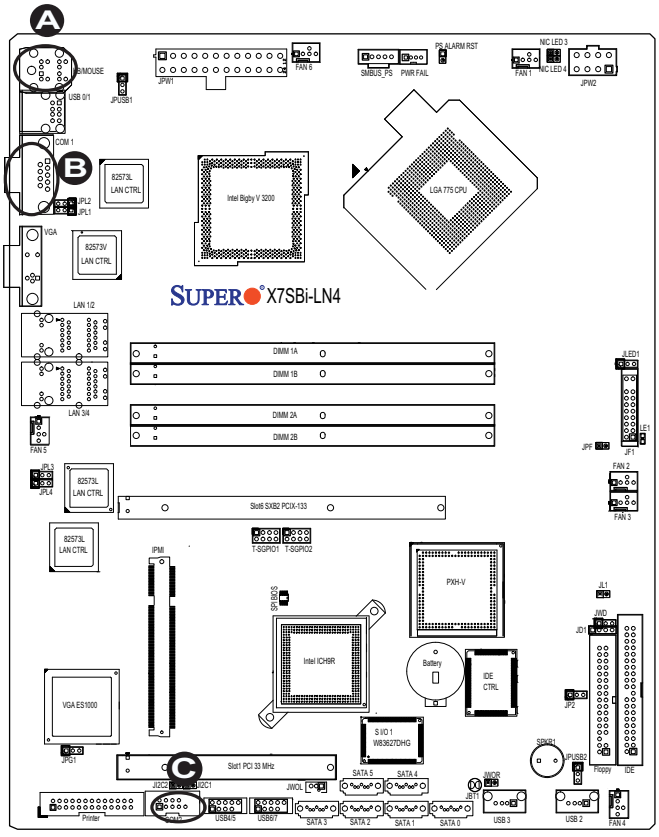
Serial Ports

COM1 is a connector located on the IO Backpanel, COM2 is a header located next to the PCI Slot. See the table on the right for pin definitions.

Serial Port Pin Definitions

Pin #	Definition	Pin #	Definition
1	DCD	6	DSR
2	RXD	7	RTS
3	TXD	8	CTS
4	DTR	9	RI
5	Ground	10	NC

(Pin 10 is available on COM2 only. NC: No Connection.)



A. Keyboard/Mouse
B. COM1
C. COM2

Power LED

The Power LED connector is located at JLED. This connection is used to provide LED Indication of power being supplied to the system. See the table on the right for pin definitions.

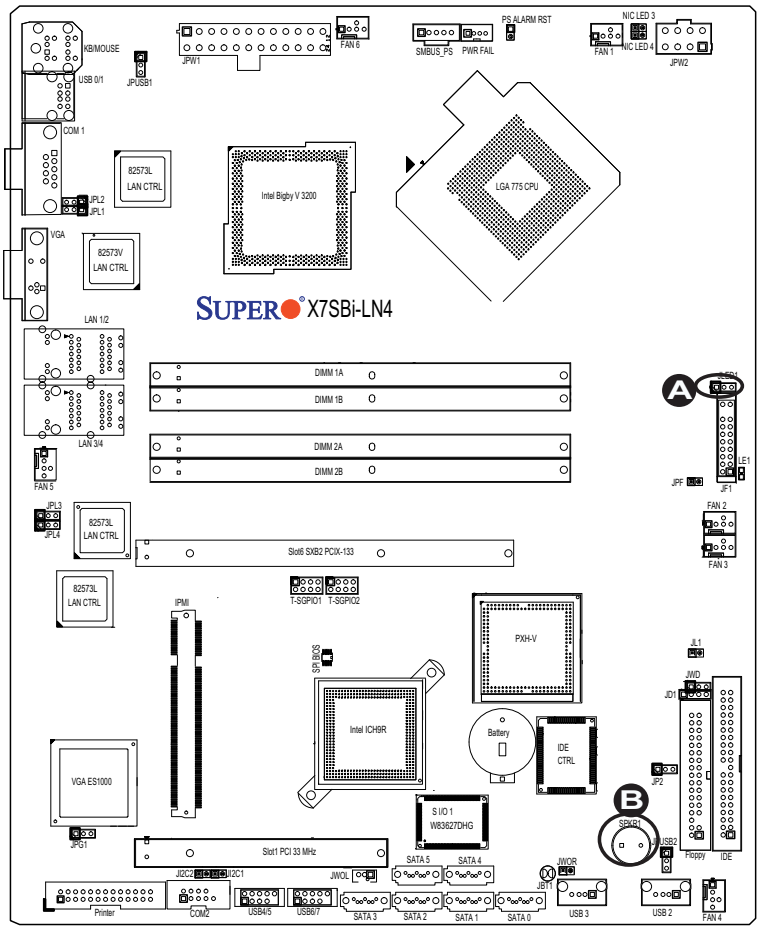
PWR LED Pin Definitions	
Pin#	Definition
1	+5V
2	Key
3	Ground

External Speaker/Internal Buzzer

On the J9 header, pins 1-4 are for an External Speaker and pins 3-4 are for the Internal Buzzer See the table on the right for speaker pin definitions.

Speaker Connector	
Pin Setting	Definition
Pins 3-4	Internal Speaker
Pins 1-4	External Speaker

Note: *Connect a cable to pins 1-4 to user an external speaker. If you wish to use the onboard buzzer, you should close pins 3-4 with a cap.*

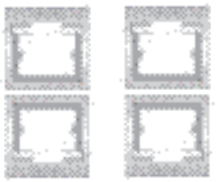


A. PWR LED

B. Speaker

GLAN (Giga-bit Ethernet Ports)

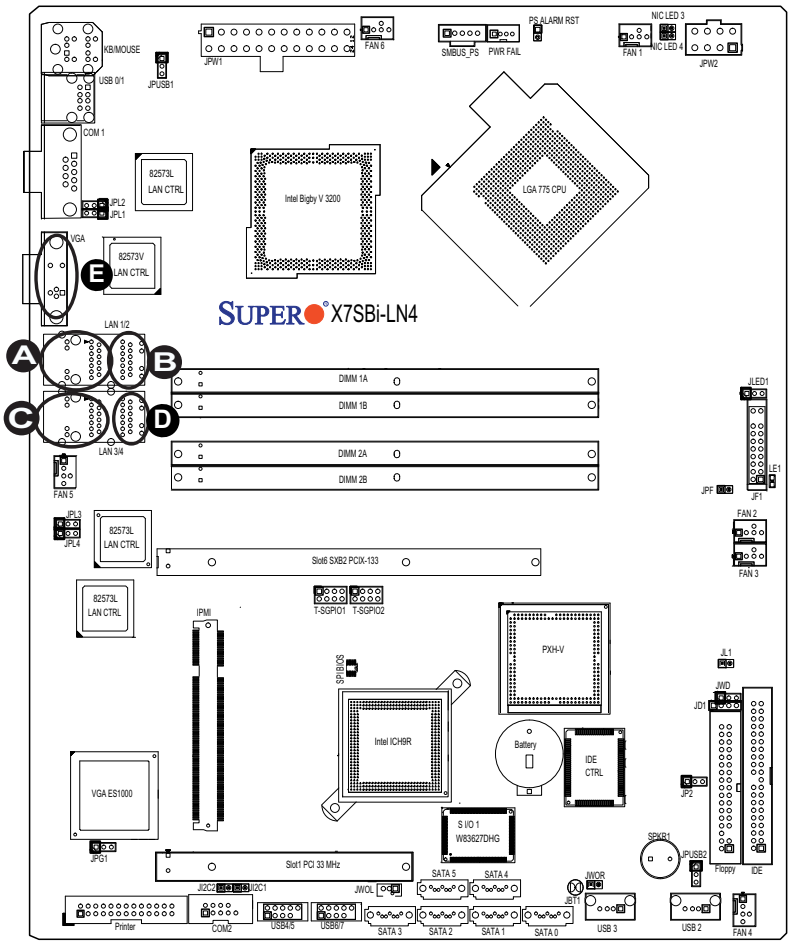
Four G-bit Ethernet ports (GLAN1/2/3/4) are located next to the VGA Connector on the IO back-plane. These ports accept RJ45 type cables.



GLAN1 GLAN3
GLAN2 GLAN4

VGA Connector

A VGA connector is located between COM1 and GLAN1 on the IO back-plane. Refer to the board layout below for the location.



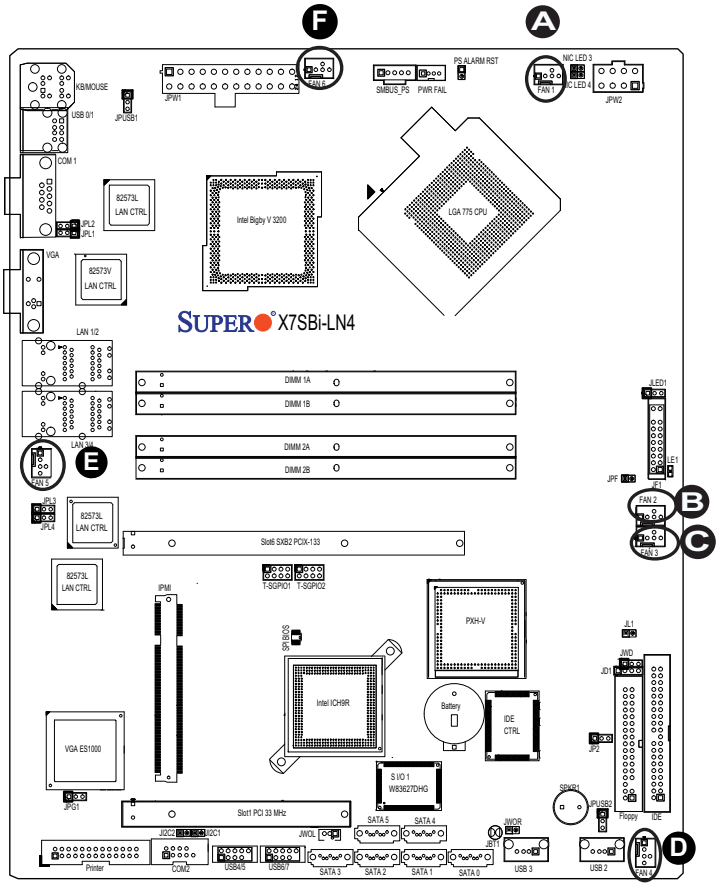
A. GLAN1
B. GLAN2
C. GLAN3
D. GLAN4
E. VGA

Fan Headers

The X7SBi-LN4 has six fan headers including five chassis/system fan headers (Fan1 to Fan5) and one CPU Fan (CPU Fan6). All these fans are 4-pin fans. However, Pins 1-3 of the fan headers are backward compatible with the traditional 3-pin fans. See the table on the right for pin definitions. The onboard fan speeds are controlled by Thermal Management via BIOS Hardware Monitoring in the Advanced Setting.

4-pin Fan Header Pin Definitions (Fan1-6)	
Pin#	Definition
1	Ground
2	+12V
3	Tachometer
4	PWR Modulation

Note: *The Default setting is Disabled.*



A. Fan 1
B. Fan 2
C. Fan 3
D. Fan 4
E. Fan 5
F. Fan 6 (CPU Fan)

Wake-On-Ring

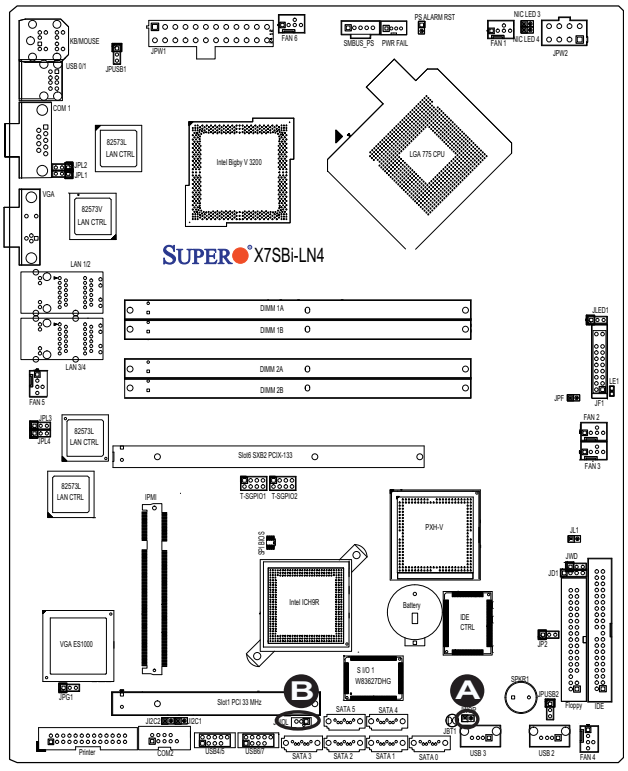
The Wake-On-Ring header is located at JWOR. This feature allows your computer to be awakened by an incoming call to the modem when the system is in the suspend state. See the table on the right for pin definitions. You must have a Wake-On-Ring card and a cable to use this feature.

Wake-On-Ring Pin Definitions (JWOR)	
Pin#	Definition
1	Ground
2	Wake-up

Wake-On-LAN

The Wake-On-LAN header is located at JWOL on the motherboard. See the table on the right for pin definitions. (You must also have a LAN card with a Wake-On-LAN connector and cable to use this feature.)

Wake-On-LAN Pin Definitions (JWOL)	
Pin#	Definition
1	+5V Standby
2	Ground
3	Wake-up



A. WOR

B. WOL

Power Fault (PWR Supply Failure)

Connect a cable from your power supply to the Power Fail (J3P) header to provide a warning in the event of a power supply failure. This warning signal is passed through the PWR_LED pin to indicate of a power failure on the chassis. See the table on the right for pin definitions.

PWR Supply Fail Pin Definitions

Pin#	Definition
1	PWR 1: Fail
2	PWR 2: Fail
3	PWR 3: Fail
4	Signal: Alarm Reset

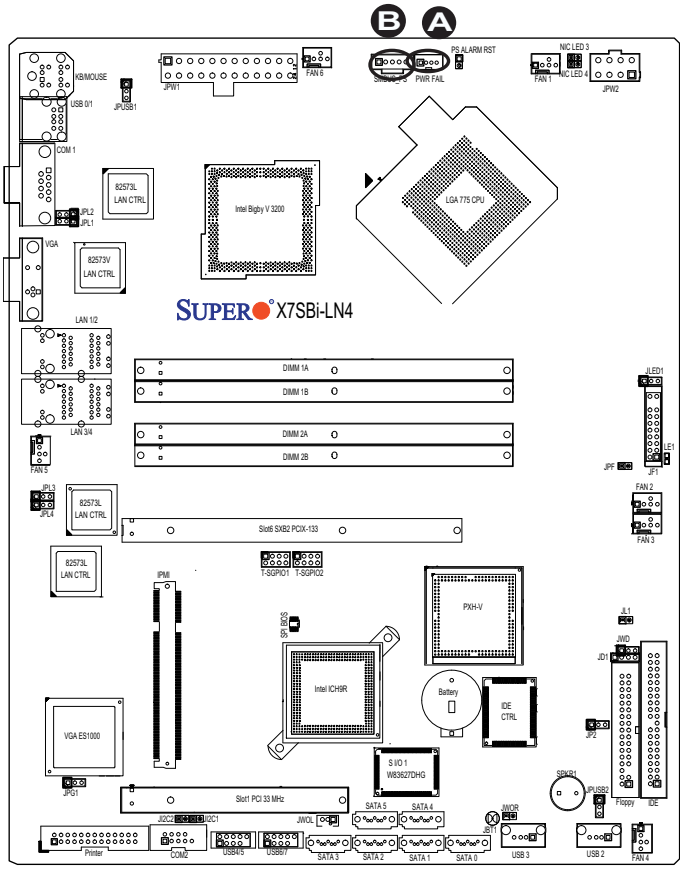
Note: *This feature is only available when using Supermicro redundant power supplies.*

Power SMB (I²C) Connector

Power SMB (**I²C**) Connector (PW4) is used to monitor PWR supply, fan and system temperature. See the table on the right for pin definitions.

PWR SMB Pin Definitions

Pin#	Definition
1	Clock
2	Data
3	PWR Fail
4	Ground
5	+3.3V



A. PWR Fault

B. PWR SMB

Chassis Intrusion

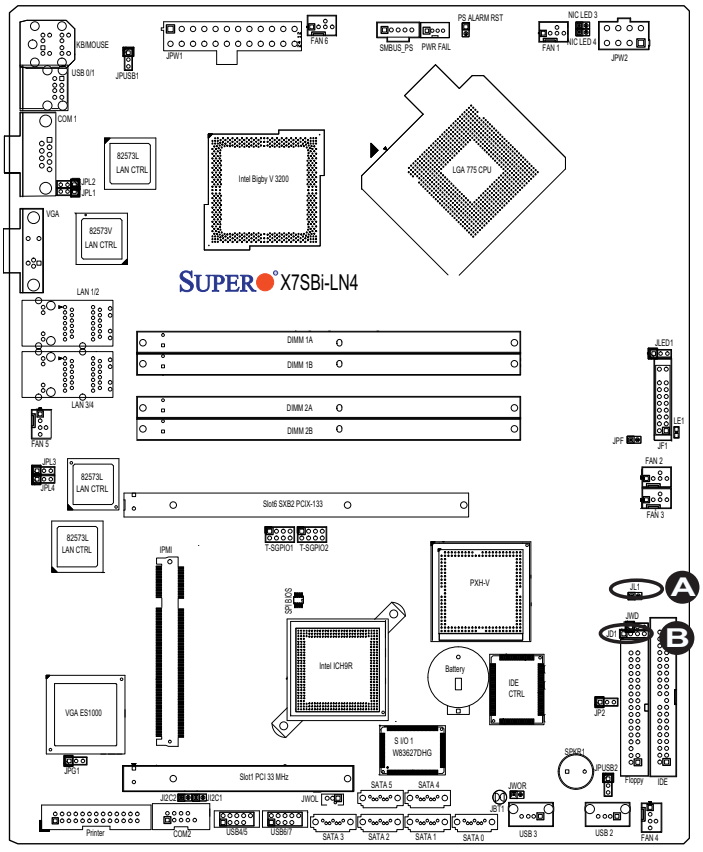
A Chassis Intrusion header is located at JL1 on the motherboard. Attach an appropriate cable from the chassis to inform you of a chassis intrusion when it is opened.

Chassis Intrusion Pin Definitions (JL1)	
Pin#	Definition
1	Intrusion Input
2	Ground

Speaker

The Speaker Header (JD1) allows you to choose between using an external speaker or the internal (onboard) speaker. Close Pins 3-4 to use the internal speaker. Connect a cable to Pins 1-4 to use an external speaker.

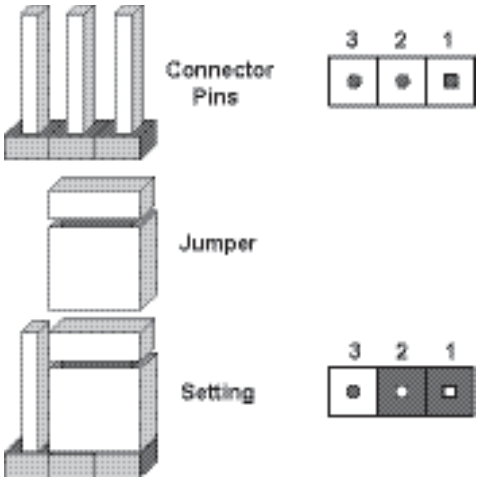
Speaker Connector Pin Definitions (JD1)	
Pin#	Definition
Pins 3-4	Internal Speaker
Pins 1-4	External Speaker



A. Chassis Intrusion

B. Speaker Header

2-7 Jumper Settings



Explanation of Jumpers

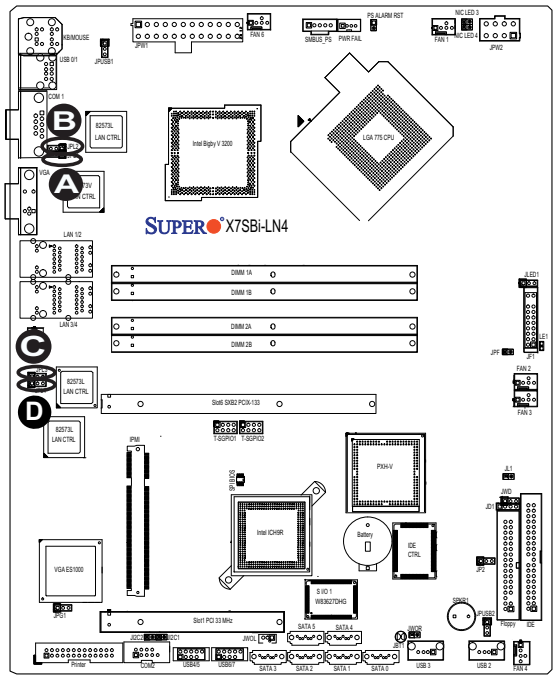
To modify the operation of the motherboard, jumpers can be used to choose between optional settings. Jumpers create shorts between two pins to change the function of the connector. Pin 1 is identified with a square solder pad on the printed circuit board. See the motherboard layout pages for jumper locations.

Note: *On two pin jumpers, "Closed" means the jumper is on and "Open" means the jumper is off the pins.*

GLAN Enable/Disable

The jumpers JPL1/JPL2/JPL3/JPL4 enable or disable the GLAN Ports 1-4 on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

GLAN Enable Jumper Settings (JPL1/ JPL2/JPL3/JPL4)	
Pin#	Definition
1-2	Enabled (default)
2-3	Disabled



A. GLAN Port1 Enable
B. GLAN Port2 Enable
C. GLAN Port3 Enable
D. GLAN Port4 Enable

Clear CMOS

JBT1 is used to clear CMOS. Instead of pins, this "jumper" consists of contact pads to prevent the accidental clearing of CMOS. To clear CMOS, use a metal object such as a small screwdriver to touch both pads at the same time to short the connection. Always remove the AC power cord from the system before clearing CMOS.

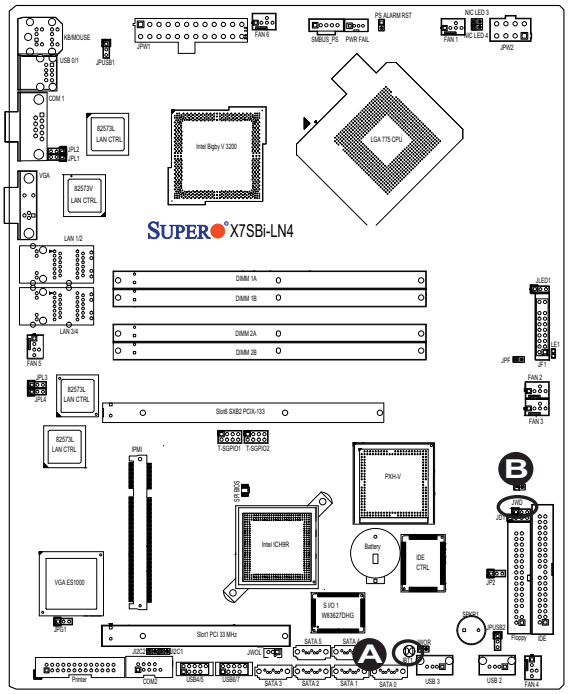
Note: *For an ATX power supply, you must completely shut down the system, remove the AC power cord and then close pins 1 and 2 to clear CMOS.*

Watch Dog Enable/Disable

JWD controls the Watch Dog function. Watch Dog is a system monitor that can reboot the system when a software application hangs. Close pins 1-2 to reset the system if an application hangs. Close pins 2-3 to generate a non-maskable interrupt signal for the application that hangs. See the table on the right for jumper settings. Watch Dog must also be enabled in the BIOS.

Note: *When enabled, the user needs to write his/her own application software to disable the Watch Dog Timer.*

Watch Dog Jumper Settings (JWD)	
Jumper Setting	Definition
Pins 1-2	Reset (*default)
Pins 2-3	NMI
Open	Disabled



A. Clear CMOS

B. Watch Dog Enable

SMB to PCI/PCI-E Slot Speeds

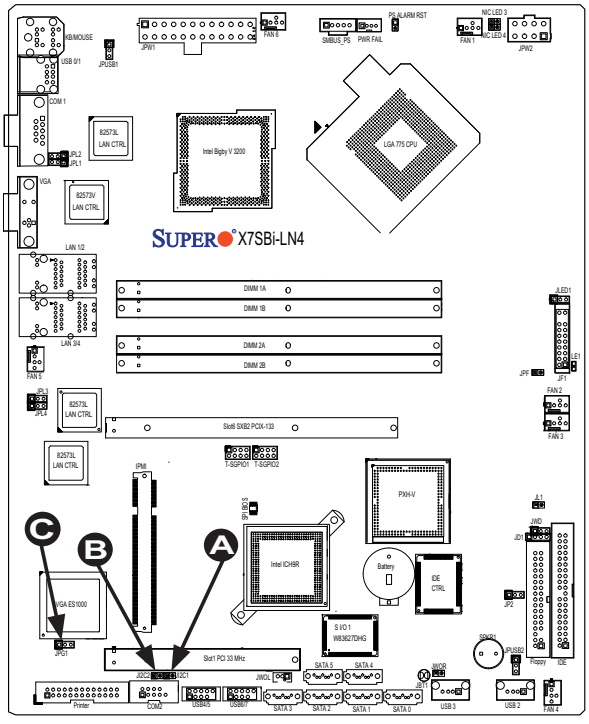
Jumpers JI²C1/JI²C2 allow you to connect PCI/PCI-Exp. Slots to the System Management Bus. The default setting is open to disable the connection. See the table on the right for jumper settings.

SMBus to PCI-X/PCI-Exp Slots Jumper Settings (JI²C1/JI²C2)	
Jumper Setting	Definition
Closed	Enabled
Open	Disabled (*Default)

VGA Enable/Disable

JPG1 enables or disables the VGA Connector on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

VGA Enable Pin Definitions (JPG1)	
Pin#	Definition
Pins 1-2	Enabled (*default)
Pins 2-3	Disabled



A. JI²C1
B. JI²C2
C. JPG1

IDE Enable/Disable

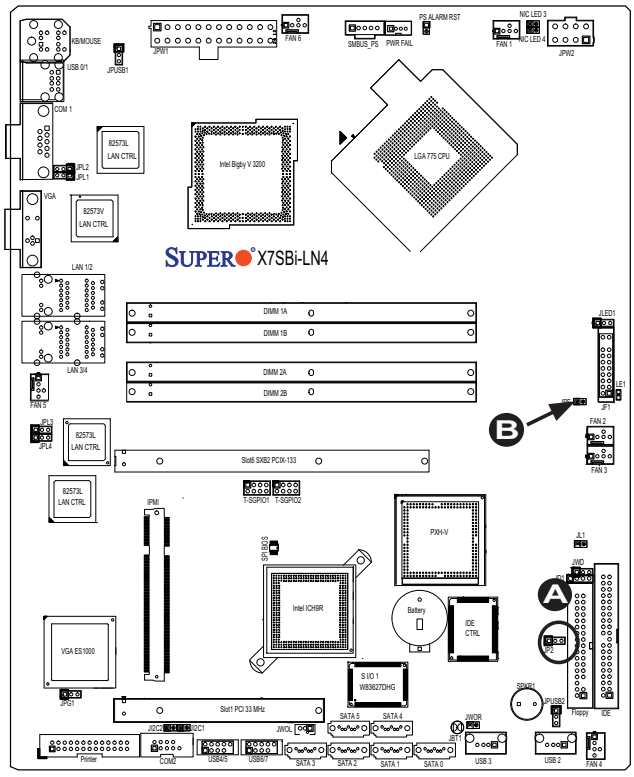
JP2 enables or disables IDE on the motherboard. See the table on the right for jumper settings. The default setting is enabled.

IDE Enable (JP2)	
Pin#	Definition
1-2	Enabled (default)
2-3	Disabled

Force-Power-On Enable/Disable

Jumper JPF allows you to enable or disable the function of Force-Power-On. If enabled, the power will always stay on automatically. If this function disabled, the user needs to press the power button to power on the system.

Power Force On Jumper Settings (JPF)	
Pin#	Definition
Off	Normal
On	Force On



A. IDE Enable

B. Power Force On

USB Wake-Up

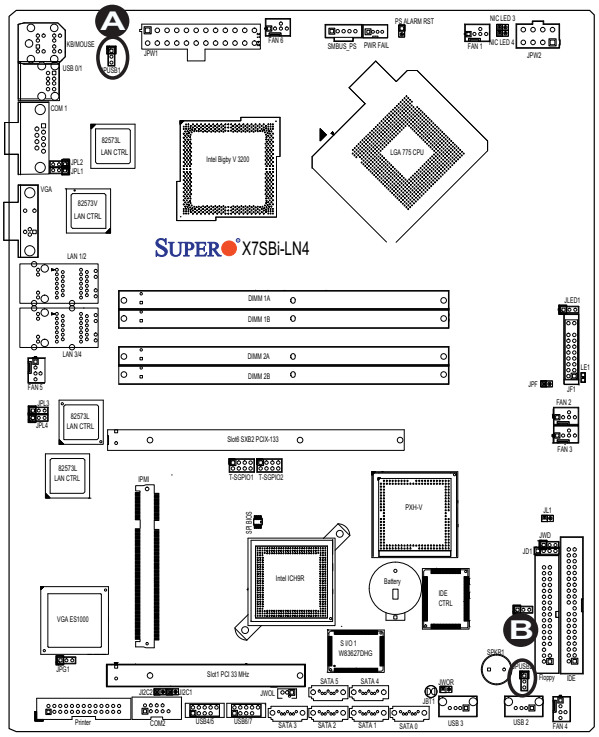
Use JPUSB jumpers to enable the function of "System Wake-Up via USB devices", which allows you to "wake-up" the system by pressing a key on the USB keyboard or by clicking the USB mouse of your system. The JPUSB jumpers are used together with the USB Wake-Up function in the BIOS. Enable both the jumpers and the BIOS setting to allow the system to "wake-up via USB Devices". See the table on the right for jumper settings.

JPUSB1 (Back Panel USB Wake-up)	
Pin#	Definition
1-2	Enabled (default)
2-3	Disabled

JPUSB2 (Front Panel USB Wake-up)	
Pin#	Definition
1-2	Enabled
2-3	Disabled (default)

Note: *JPUSB1 is for Back Panel USB ports:0/1, and JPUSB2 is for Front Panel USB ports:4/5/6/7/8/9.*

The default jumper setting for the USB ports located on the back panel is "Enabled". However, when the "USB Wake-Up" function is enabled in the BIOS and the desired USB ports are enabled via the JPUSB jumper, please be sure to remove all USB devices from the USB ports whose USB jumpers are set to "Disabled" before the system goes into the standby mode.



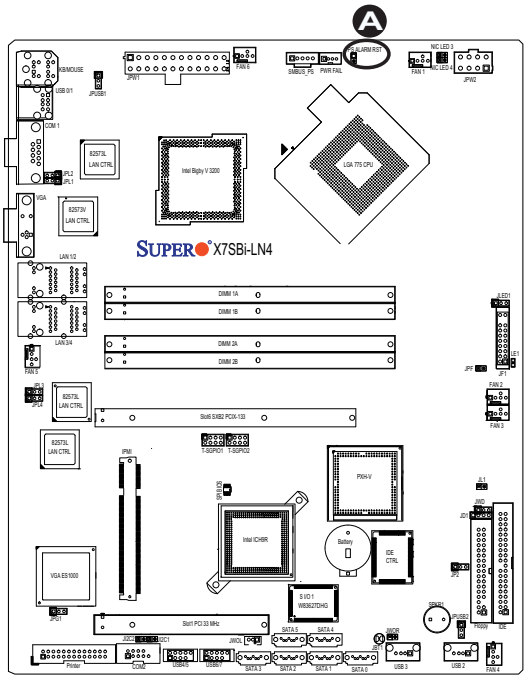
A. JPUSB1

B. JPUSB2

Redundant PWR Alarm Reset

A Redundant Power Supply Fail Alarm Reset is located at JAR. The system will notify you in the event of a power supply failure. If you only have a single power supply installed, you should not connect anything to this jumperto prevent false alarms. Refer to the board layout below for the location.

Redundant PWR Jumper Settings	
Pins	Settings
1-2	Reset
2-3	Normal (default)

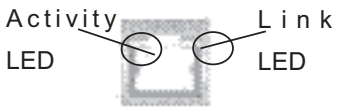


A. Alarm Reset

2-8 Onboard Indicators

GLAN LEDs

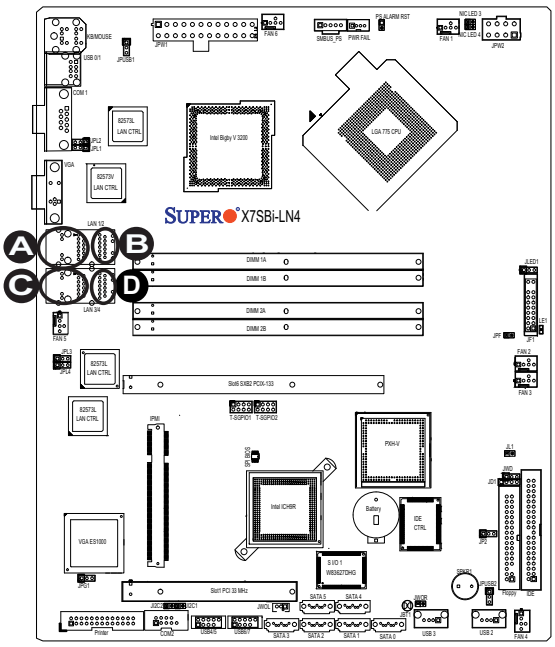
There are four GLAN ports on the motherboard. Each Gigabit Ethernet LAN port has two LEDs. The green LED indicates activity, while the Link LED may be green, amber or off to indicate the speed of the connection. See the tables at right for more information.



(**Rear View**: When viewing it from the rear side of the chassis)

GLAN Activity Indicator Status		
Color	Status	Definition
Green	Flashing	Active

GLAN Link Indicator Status	
LED Color	Definition
Off	No Connection or 10 Mbps
Green	100 Mbps
Amber	1 Gbps

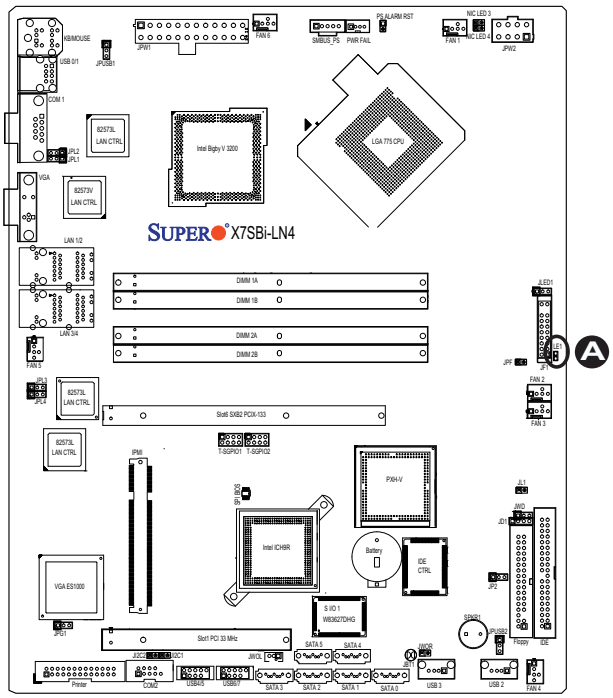


A. GLAN1
B. GLAN2
C. GLAN3
D. GLAN4

Onboard Power LED (LE1)

The Onboard Standby Power LED is located at LE1 on the motherboard. When LE1 is off, the system is off. When the green light is on, the system is on. When the LED is on, the power is on. Unplug the power cable before removing or installing components. See the layout below for the LED location.

Onboard PWR LED Indicator (LE1)	
LED Color	Definition
Off	System Off
On	Standby Power On
Green	System On



A. Power LED

2-9 Floppy, Parallel and SIMSO Connections

Note the following when connecting the floppy and hard disk drive cables:

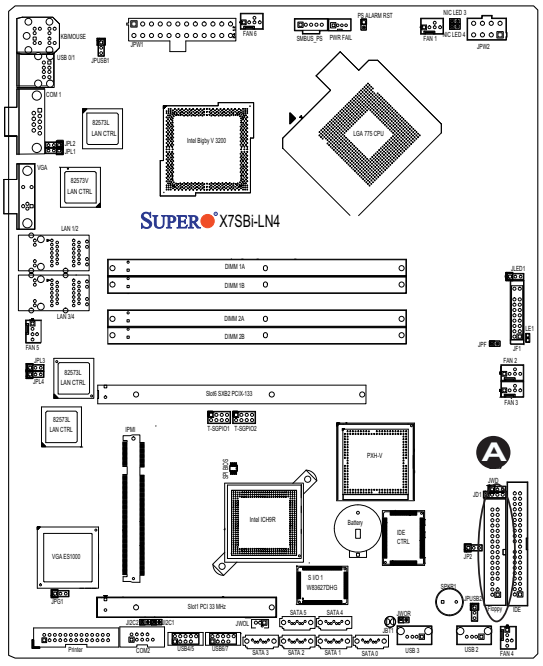
- The floppy disk drive cable has seven twisted wires.
- A red mark on a wire typically designates the location of pin 1.
- A single floppy disk drive ribbon cable has two connectors to provide for two floppy disk drives. The connector with twisted wires always connects to drive A, and the connector that does not have twisted wires always connects to drive B.

Floppy Connector

The floppy connector is located at J1005. See the table below for pin definitions.

Floppy Drive Connector Pin Definitions (Floppy)

Pin#	Definition	Pin #	Definition
1	Ground	2	FDHDIN
3	Ground	4	Reserved
5	Key	6	FDEDIN
7	Ground	8	Index
9	Ground	10	Motor Enable
11	Ground	12	Drive Select B
13	Ground	14	Drive Select B
15	Ground	16	Motor Enable
17	Ground	18	DIR
19	Ground	20	STEP
21	Ground	22	Write Data
23	Ground	24	Write Gate
25	Ground	26	Track 00
27	Ground	28	Write Protect
29	Ground	30	Read Data
31	Ground	32	Side 1 Select
33	Ground	34	Diskette



A. Floppy

Parallel Port Header

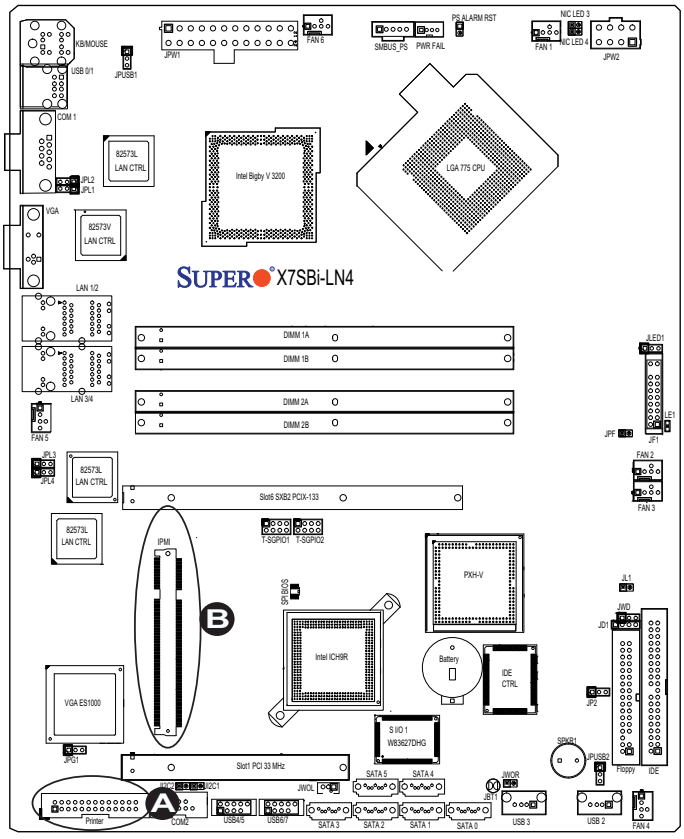
The parallel (printer) header is located on the Backplane IO Panel. See the table on the right for pin definitions.

Parallel (Printer) Connector Pin Definitions

Pin#	Definition	Pin #	Definition
1	Strobe-	2	Auto Feed-
3	Data Bit 0	4	Error-
5	Data Bit 1	6	Init-
7	Data Bit 2	8	SLCT IN-
9	Data Bit 3	10	GND
11	Data Bit 4	12	GND
13	Data Bit 5	14	GND
15	Data Bit 6	16	GND
17	Data Bit 7	18	GND
19	ACK	20	GND
21	BUSY	22	Write Data
23	PE	24	Write Gate
25	SLCT	26	NC

SIMSO IPMI 2.0 Socket

A SIMSO IPMI 2.0 Socket is located next to the PCI slots on the motherboard. This connection provides IPMI (Intelligent Power Management Interface) capability to the motherboard. Refer to the layout below for the SIMSO IPMI location.



A Parallel Header

B.SIMSO IPMI 2.0

Notes

Chapter 3

Troubleshooting

3-1 Troubleshooting Procedures

Use the following procedures to troubleshoot your system. If you have followed all of the procedures below and still need assistance, refer to the 'Technical Support Procedures' and/or 'Returning Merchandise for Service' section(s) in this chapter. Always disconnect the AC power cord before adding, changing or installing any hardware components.

Before Power On

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Disconnect all ribbon/wire cables from the motherboard, including those for the keyboard and mouse.
3. Remove all add-on cards.
4. Install a CPU and heatsink (making sure it is fully seated) and connect the chassis speaker and the power LED to the motherboard. Check all jumper settings as well.
5. Use the correct type of onboard CMOS battery as specified by the Manufacturer. Do not install the CMOS battery upside down to avoid possible explosion.

No Power

1. Make sure that there are no short circuits between the motherboard and chassis.
2. Verify that all jumpers are set to their default positions.
3. Check that the 115V/230V switch on the power supply is properly set.
4. Turn the power switch on and off to test the system.

5. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

No Video

1. If the power is on but you have no video, remove all the add-on cards and cables.

2. Use the speaker to determine if any beep codes exist. Refer to Appendix A for details on beep codes.

Note: *If you are a system integrator, VAR or OEM, a POST diagnostics card is recommended. For I/O port 80h codes, refer to App. B.*

Memory Errors

1. Make sure that the DIMM modules are properly and fully installed.

2. You should be using unbuffered DDR2 memory (see the next page). Also, it is recommended that you use the same memory speed for all DIMMs in the system. See Section 2-4 for memory limitations.

3. Check for bad DIMM modules or slots by swapping modules between slots and noting the results.

4. Check the power supply voltage 115V/230V switch.

Losing the System's Setup Configuration

1. Make sure that you are using a high quality power supply. A poor quality power supply may cause the system to lose the CMOS setup information.

2. The battery on your motherboard may be old. Check to verify that it still supplies ~3VDC. If it does not, replace it with a new one.

3. If the above steps do not fix the Setup Configuration problem, contact your vendor for repairs.

3-2 Technical Support Procedures

Before contacting Technical Support, please take the following steps. Also, note that as a motherboard manufacturer, Supemicro does not sell directly to end-users, so it is best to first check with your distributor or reseller for troubleshooting services. They should know of any possible problem(s) with the specific system configuration that was sold to you.

1. Please go through the 'Troubleshooting Procedures' and 'Frequently Asked Question' (FAQ) sections in this chapter or see the FAQs on our web site (http://www.supermicro.com/support/faqs/) before contacting Technical Support.

Notes: *1. Not all BIOS can be flashed; it depends on the modifications to the boot block code.*

2. The onboard SPI BIOS chip cannot be removed from the motherboard. To repair or replace the BIOS chip, please return the motherboard to RMA Dept. at Supermicro for service.

2. BIOS upgrades can be downloaded from our web site at (http://www.supermicro com/support/bios/).

3. If you still cannot resolve the problem, include the following information when contacting Supermicro for technical support:

   - Motherboard model and PCB revision number
   - BIOS release date/version (this can be seen on the initial display when your system first boots up).
   - System configuration
   - An example of a Technical Support form is on our web site at (http://www.supermicro.com/support/contact.cfm).

4. Distributors: For immediate assistance, please have your account number ready when placing a call to our technical support department. We can be reached by e-mail at support@supermicro.com, by phone at:(408) 503-8000, option 2, or by fax at (408) 503-8019.

3-3 Frequently Asked Questions

Question: What type of memory does my motherboard support?

Answer: The X7SBi-LN4 supports unbuffered, ECC/Non-ECC DDR2 667/800 MHz memory modules. See Section 2-4 for details on installing memory.

Question: How do I update my BIOS?

Answer: It is recommended that you not upgrade your BIOS if you are not experiencing problems with your system. Updated BIOS files are located on our web site at (http://www.supermicro com/support/bios/). Please check our BIOS warning message and the information on how to update your BIOS on our web site. Also, check the current BIOS revision and make sure it is newer than your current BIOS before downloading. Select your mainboard model and download the BIOS file to your computer. Unzip the BIOS update file and follow the readme.txt to flash the BIOS. Question: What's on the CD that came with my motherboard?

Answer: The supplied compact disc has quite a few drivers and programs that will greatly enhance your system. We recommend that you review the CD and install the applications you need. Applications on the CD include chipset drivers for Windows and security and audio drivers.

Question: After I have installed 4 pieces of 1GB Memory, why does the BIOS only detect about 3.145 GB of memory during POST?

Answer: Because the chipset does not support memory remapping, and PCI-E memory requires a great deal of memory, so there is a memory hole located around the 4GB memory address.

3-4 Returning Merchandise for Service

A receipt or copy of your invoice marked with the date of purchase is required before any warranty service will be rendered. You can obtain service by calling your vendor for a Returned Merchandise Authorization (RMA) number. When returning to the manufacturer, the RMA number should be prominently displayed on the outside of the shipping carton, and mailed prepaid or hand-carried. Shipping and handling charges will be applied for all orders that must be mailed when service is complete.

This warranty only covers normal consumer use and does not cover damages incurred in shipping or from failure due to the alteration, misuse, abuse or improper maintenance of products.

During the warranty period, contact your distributor first for any product problems.

Chapter 4

BIOS

4-1 Introduction

This chapter describes the Phoenix BIOS™ Setup utility for the X7SBi-LAN4. The Phoenix ROM BIOS is stored in a flash chip and can be easily upgraded using a floppy disk-based program.

Note: Due to periodic changes to the BIOS, some settings may have been added or deleted and might not yet be recorded in this manual. Please refer to the Manual Download area of the Supermicro web site <http://www.supermicro.com> for any changes to the BIOS that may not be reflected in this manual.

System BIOS

BIOS is the Basic Input Output System used in all IBM® PC, XT™, AT®, and PS/2® compatible computers. The Phoenix BIOS stores the system parameters, types of disk drives, video displays, etc. in the CMOS. The CMOS memory requires very little electrical power. When the computer is turned off, a backup battery provides power to the CMOS logic, enabling it to retain system parameters. Each time when the computer is powered on, the computer is configured with the values stored in the CMOS logic by the system BIOS, which gains control at boot up.

How To Change the Configuration Data

The CMOS information that determines the system parameters may be changed by entering the BIOS Setup utility. This Setup utility can be accessed by pressing the <Delete> key at the appropriate time during system boot. Normally, the only visible POST (Power On Self Test) routine is the memory test. As the memory is being tested, press the <Delete> key to enter the main menu of the BIOS Setup utility. From the main menu, you can access the other setup screens. Beginning with Section 4-3, detailed descriptions are given for each parameter setting in the Setup utility.

4-2 Running Setup

Default settings are in bold text unless otherwise noted.

The BIOS setup options described in this section are selected by choosing the appropriate text from the main BIOS Setup screen. All displayed text is described in this section, although the screen display is often all you need to understand how to set the options (See the next page).

When you first power on the computer, the Phoenix BIOS™ is immediately activated.

While the BIOS is in control, the Setup program can be activated in one of two ways:

1. By pressing <Delete> immediately after turning the system on, or

2. When the message shown below appears briefly at the bottom of the screen during the POST (Power On Self-Test), press the <Delete> key to activate the main Setup menu.

Press the <Delete> key to enter Setup

4-3 Main BIOS Setup

All main Setup options are described in this section. The main BIOS Setup screen displays below.

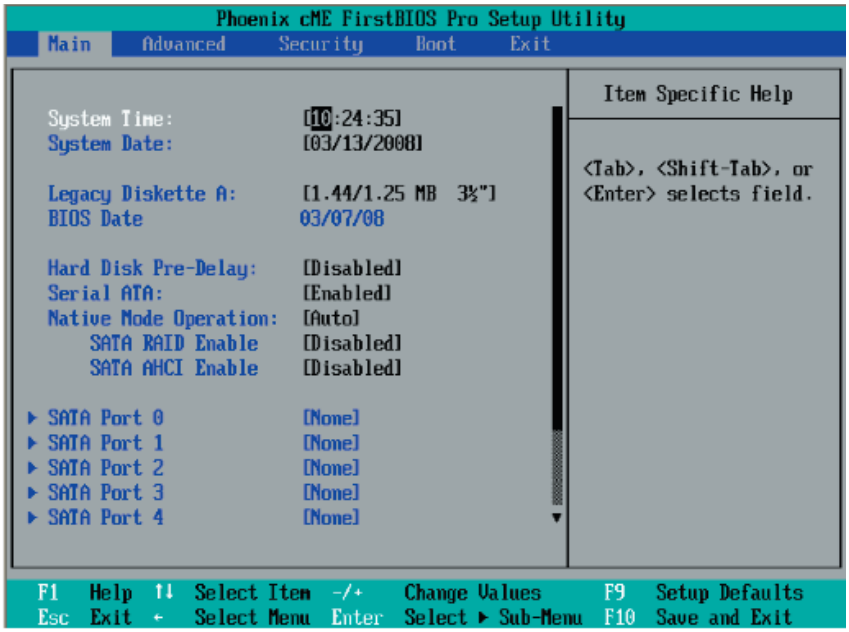
Use the Up/Down arrow keys to move between the different settings in each menu. Use the Left/Right arrow keys to change the options for each setting.

Press the <Esc> key to exit the BIOS Setup Menu. The next section describes in detail how to navigate through the menus.

Items that use submenus are indicated with the ▶icon. With the item highlighted, press the <Enter> key to access the submenu.

Warning: Do not shut down or reset the system while updating the BIOS to prevent possible boot failure.

Main BIOS Setup Menu



Main Setup Features

System Time

To set the system date and time, key in the correct information in the appropriate fields. Then press the <Enter> key to save the data.

System Date

Using the arrow keys, highlight the month, day and year fields, and enter the correct data. Press the <Enter> key to save the data.

Legacy Diskette A

This setting allows the user to set the type of floppy disk drive installed as diskette A. The options are Disabled, 360Kb 5.25 in, 1.2MB 5.25 in, 720Kb 3.5 in, **1.44/1.25MB**, 3.5 in and 2.88 MB 3.5 in.

BIOS Date

The item displays the date that this version of BIOS was built.

Hard Disk Pre-Delay

This setting allows the user to add a delay before a hard drive is accessed by the BIOS for the first time. This delay will allow the time needed for the hard drive to be properly initialized before it is accessed by the BIOS to prevent a possible boot failure. The options are: **Disabled**, 3 Seconds, 6 Seconds, 9 Seconds,15 Seconds, 21 Seconds, and 30 Seconds.

Serial ATA

This setting allows the user to enable or disable Serial ATA connections. The options are Disabled and **Enabled.**

Native Mode Operation

Select Serial ATA for SATA or select Auto (Native Mode) for ATA. The options are: Serial ATA and Auto.

Serial ATA (SATA) RAID Enable

Select Enable to enable Serial ATA RAID functions. (For the Windows OS environment, use the RAID driver if this feature is set to Enabled. When set to Enabled, the item: ICH RAID Code Base will become available. If this item is set to Disabled, the item-SATA AHCI Enable will be available.) The options are Enabled and Disabled.

ICH RAID Code Base

Select Intel to enable the Intel SATA Host RAID Utility. Select Intel to use the Intel HostRAID Utility. The default setting is Intel.

SATA AHCI

Select Enable to enable the function of Serial ATA Advanced Host Interface. (Take caution when using this function. This feature is for advanced programmers only.The Enhanced AHCI mode is available when the Windows XP-SP1 OS and the IAA Driver is used.) The options are Enabled and Disabled.

SATA AHCI Legacy Enable

This setting allows the user to enable or disable SATA ports 5 and 6 while in this mode. The options are **Disabled** and Enabled.

▶ SATA Port0/SATA Port1/SATA Port2/SATA Port3/SATA Port4/SATA Port5/Ext. Primary Master/Ext. Primary Slave

These settings allow the user to set the parameters of SATA Port0/SATA Port1/SATA Port2/SATA Port3/SATA Port4/SATA Port5/Ext. Primary Master/Ext. Primary Slave connections. Hit <Enter> to activate the following sub-menu screen for detailed options of the items. Set the correct configurations accordingly. The items included in the sub-menu are:

Type

This feature allows the user to select the type of a drive specified. Select **Auto to** allow the BIOS to automatically set the capacity of the drive specified. Select User to allow the user to enter the parameters of the drive installed at this con-

nection. Select CDROM if a CDROM drive is installed. Select ATAPI if a removable disk drive is installed.

Multi-Sector Transfers

This item allows the user to specify the number of sectors per block to be used for multi-sector transfer. The options are **Disabled**, 4 Sectors, 8 Sectors, and 16 Sectors.

LBA Mode Control

This item determines whether the Phoenix BIOS will access the IDE Primary Master device via the LBA mode. The options are Enabled and **Disabled.**

32-Bit I/O

Select Enable to enable the function of 32-bit IDE data transfer. The options are Enabled and **Disabled**.

Transfer Mode

This option allows the user to set the transfer mode. The options are **Standard**, Fast PIO1, Fast PIO2, Fast PIO3, Fast PIO4, FPIO3/DMA1 and FPIO4/DMA2.

Ultra DMA Mode

This option allows the user to configure the Ultra DMA Mode. The options are **Disabled**, Mode 0, Mode 1, Mode 2, Mode 3, Mode 4, and Mode 5.

Installed Memory

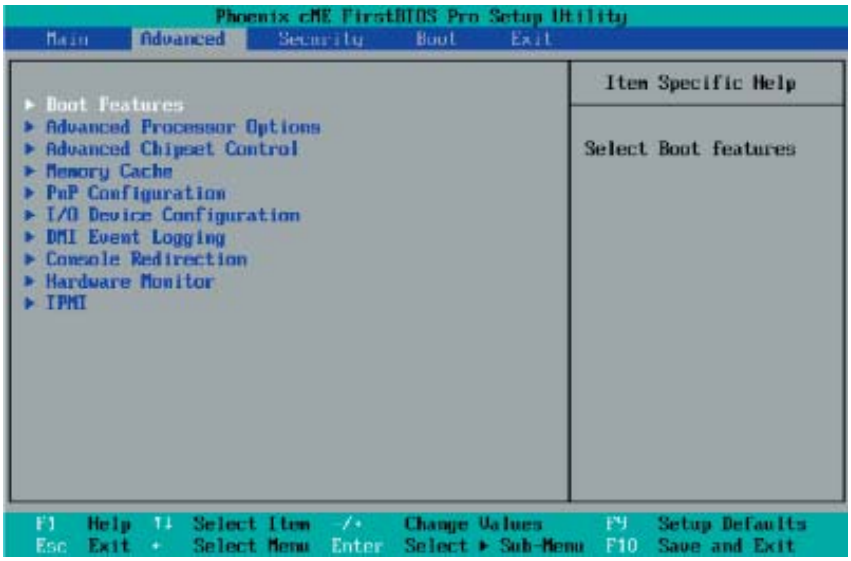
This feature informs you how much memory has been installed in the system as detected by the BIOS.

System Memory

This feature informs you how much system memory has been detected by the BIOS.

4-4 Advanced Setup

Choose Advanced from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. The items with a triangle beside them have submenus that can be accessed by highlighting the item and pressing <Enter>.



▶Boot Features

Access the submenu to make changes to the following settings.

QuickBoot Mode

If enabled, this feature will speed up the POST (Power On Self Test) routine by skipping certain tests after the computer is turned on. The settings are **Enabled** and Disabled. If Disabled, the POST routine will run at normal speed.

Quiet Mode

This setting allows you to **Enable** or Disable the graphic logo screen display at bootup.

ACPI Mode

Select **Yes** to use the ACPI (Advanced Configuration and Power Interface) power management on your system. The options are **Yes** and No.

Power Button Behavior

If set to **Enabled**, the system will power on or power off immediately as soon as the user hits the power button. The options are **Enabled** and Disabled.

Resume On Modem Ring

Select On to “wake your system up” when an incoming call is received by your modem. The options are On and **Off**.

Resume On PME#

Select On to “wake your system up” from the PME#. The options are On and **Off**.

PS2 Keyboard (KB)/Mouse Wake Up

Select Enable to “wake your system up” from the S3, S4 or S5 state. If this feature is set enabled, you will also need to enable the JPWAKE jumper by closing pins 1-2. (Please refer to Page 1-5 and Chapter 2 for more details). The default setting is **Disabled**.

Power Loss Control

This setting allows you to choose how the system will react when power returns after an unexpected power loss. The options are Stay Off, Power On, and **Last State**.

Watch Dog

If enabled, this option will automatically reset the system if it is not active for more than 5 minutes. The options are Enabled and **Disabled**.

Extended Shadow RAM

Select Enabled to enable Extended Shadow RAM and allow the contents stored in ROM to be directly copied into extended memory for faster operation. Take caution when using this feature. If the system hangs, reset the setting for this item and reboot the system. The options are Enabled and **Disabled**.

Summary Screen

This setting allows you to **Enable** or Disable the summary screen which displays the system configuration during bootup.

▶Advanced Processor Options

Access the submenu to make changes to the following settings.

CPU Speed

This is a display to indicate the speed of the CPU installed on the motherboard.

Frequency Ratio (Available when supported by the CPU)

The feature allows the user to set the internal frequency multiplier for the CPU. The options are: **Default** and x9.

Core-Multi-Processing (Available when supported by the CPU)

Set to Enabled to use a processor's Second Core and beyond. (Please refer to Intel's web site for more information.) The options are Disabled and **Enabled.**

Machine Checking (Available when supported by the CPU)

Set to Enabled to activate the function of Machine Checking and allow the CPU to detect and report hardware (machine) errors via a set of model-specific registers (MSRs). The options are Disabled and **Enabled**.

Compatible FPU Code (Available when supported by the CPU)

Set to Enabled to keep the content of the last instruction Operating Code (OPCode) in the floating point (FP) state. The options are **Disabled** and Enabled.

Thermal Management 2 (Available when supported by the CPU)

Set to **Enabled** to use Thermal Management 2 (TM2) which will lower CPU voltage and frequency when the CPU temperature reaches a predefined overheat threshold. Set to Disabled to use Thermal Manager 1 (TM1), allowing CPU clocking to be regulated via CPU Internal Clock modulation when the CPU temperature reaches the overheat threshold.

Adjacent Cache Line Prefetch (Available when supported by the CPU)

The CPU fetches the cache line for 64 bytes if this option is set to Disabled. The CPU fetches both cache lines for 128 bytes as comprised if Enabled. The options are Disabled and **Enabled.**

Set Maximum Ext. CPUID=3

When set to Enabled, the Maximum Extended CPUID will be set to 3. The options are **Disabled** and Enabled.

Echo TPR

Set to **Enabled** to prevent xTPR messages from being sent to the system. The options are Disabled and **Enabled**.

C1 Enhanced Mode (Available when supported by the CPU)

Set to Enabled to enable the Enhanced Halt State to lower CPU voltage/frequency to prevent overheat. The options are Enabled and **Disabled**. (**Note:** please refer to Intel's web site for detailed information.)

No Execute Mode Memory Protection (Available when supported by the CPU and the OS.)

Set to **Enabled** to enable Execute Disable Bit and allow the processor to classify areas in the memory slot where an application code can execute and where it cannot, and thus preventing a worm or a virus from inserting and creating a flood of codes to overwhelm the processor and damage the system during an attack.

Note: this feature is available when your OS and your CPU support the functionality of Execute Disable Bit. The options are Disabled and **Enabled**. For more information regarding hardware/software support for this function, please refer to Intel's and Microsoft's web sites.

Enhanced Intel Speed Step Support (Available when supported by the CPU)

Select Enabled to use the Enhanced Intel SpeedStep Technology and allow the system to automatically adjust processor voltage and core frequency in an effort to reduce power consumption and heat dissipation. The options are Enabled (-C States, GV1/GV3 are enabled), **GV1/GV3 Only** (C States: Disabled), C-States Only (-G1/G3: Disabled), and Disabled (-C States, GV1/GV3 are disabled). Please refer to Intel's web site for detailed information. Please refer to Intel's web site for detailed information.

▶Advanced Chipset Control

Access the submenu to make changes to the following settings.

Warning: Take caution when changing the Advanced settings. An incorrect setting, a very high DRAM frequency, or an incorrect DRAM timing may make the system to become unstable. When this occurs, reset the setting to the default setting.

Memory Remapping

Select Enable to use the functionality of Memory Remapping above 4GB. The settings are **Enabled** and Disabled.

High Precision Event Time

Select Yes to activate the High Precision Event Timer (HPET), which is capable of producing periodic interrupts at a much higher frequency than a Real-time Clock (RTC) can in synchronizing multimedia streams, providing smooth playback and reducing the dependency on other timestamp calculation devices, such as an x86 RDTSC Instruction embedded in a CPU. The High Precision Event Timer is used to replace the 8254 Programmable Interval Timer. The options for this feature are Yes and **No**.

Route Port 80h Cycles to

This feature allows the user to decide which bus to send debug information to. The options are Disabled, **PCI** and LPC.

Legacy USB Support

This setting allows you to enable support for Legacy USB devices. The settings are **Enabled** and Disabled.

USB Host Controller

Select Enabled to enable the USB Host Controller. The settings are **Enabled** and Disabled.

▶Memory Cache

Memory Cache

This setting allows the user to enable or disable the functionality of Memory Cache. The options are **Enabled** and Disabled.

Cache System BIOS Area

This setting allows you to designate a reserve area in the system memory to be used as a System BIOS buffer to allow the BIOS to write (cache) its data into this reserved memory area. Select **Write Protect** to enable this function, and this area will be reserved for BIOS ROM access only. Select Uncached to disable this function and make this area available for other devices.

Cache Video BIOS Area

This setting allows you to designate a reserve area in the system memory to be used as a Video BIOS buffer to allow the BIOS to write (cache) data into this reserved memory area. Select **Write Protect** to enable the function and this area will be reserved for Video BIOS ROM access only. Select Uncached to disable this function and make this area available for other devices.

Cache Base 0-512K

If enabled, this feature will allow the data stored in the base memory area of block 0-512K to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or to be written into L1, L2 cache inside the CPU to speed up CPU operations .Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 0-512K. Select Write Back to allow the CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are Uncached, Write Through, Write Protect, and **Write Back**.

Cache Base 512K-640K

If enabled, this feature will allow the data stored in the memory area of 512K-640K to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or written into L1, L2 or L3 cache inside the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to

be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the base memory area of Block 512K-640K. Select Write Back to allow the CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are Uncached, Write Through, Write Protect, and **Write Back**.

Cache Extended Memory

If enabled, this feature will allow the data stored in the extended memory area to be cached (written) into a buffer, a storage area in the Static DROM (SDROM) or written into L1, L2 or L3 cache inside the CPU to speed up CPU operations. Select Uncached to disable this function. Select Write Through to allow data to be cached into the buffer and written into the system memory at the same time. Select Write Protect to prevent data from being written into the extended memory area above 1 MB. Select Write Back to allow the CPU to write data back directly from the buffer without writing data to the System Memory for fast CPU data processing and operation. The options are Uncached, Write Through, Write Protect, and **Write Back**.

Discrete MTRR Allocation

If enabled, MTRRs (-Memory Type Range Registers) are configured as distinct, separate units and cannot be overlapped. If enabled, the user can achieve better graphic effects when using a Linux graphic driver that requires the write-combining configuration with 4GB or more memory. The options are Enabled and **Disabled**.

▶PnP Configuration

Access the submenu to make changes to the following settings for PCI devices.

▶PCI 32 Slot

Access the submenu for each of the settings above to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are Enabled and **Disabled**.

Latency Timer

This setting allows you to set the clock rate for the Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are

Default, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate**.**

PCIx(s) Frequency

This setting allows you to change the bus frequency for the slots to run. The options are **Auto**, PCI 33 MHz, PCI 66 MHz, PCI-X 66 MHz, PCI-X 100 MHz, and PCI-X 133 MHz,.

▶PCI-X 133 MHz Slot

Access the submenu to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are Enabled and **Disabled.**

Latency Timer

This setting allows you to set the clock rate for the Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For the Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate**.**

▶Onboard LAN1/LAN2/LAN3/LAN4

Access the submenu to make changes to the following:

Option ROM Scan

When enabled, this setting will initialize the device expansion ROM. The options are **Enabled** and Disabled.

Enable Master

This setting allows you to enable the selected device as the PCI bus master. The options are Enabled and **Disabled.**

Latency Timer

This setting allows you to set the clock rate for the Bus Master. A high-priority, high-throughout device may benefit from a greater clock rate. The options are **Default**, 0020h, 0040h, 0060h, 0080h, 00A0h, 00C0h, and 00E0h. For the Unix, Novelle and other Operating Systems, please select the option: other. If a drive fails after the installation of a new software, you might want to change this setting and try again. A different OS requires a different Bus Master clock rate**.**

▶I/O Device Configuration

Access the submenu to make changes to the following settings.

KBC Clock Input

This setting allows you to set a clock frequency for the KBC. The options are 6MHz, 8MHz, **12MHz,** and 16MHz.

Serial Port A

This setting allows you to decide how Serial Port A will be controlled. The options are **Enabled** (user defined), Disabled, and Auto (BIOS- or OS- controlled).

Base I/O Address

This setting allows you to select the base I/O address for Serial Port A. The options are **3F8**, 2F8, 3E8, and 2E8.

Interrupt

This setting allows you to select the IRQ (interrupt request) for Serial Port A. The options are IRQ3 and **IRQ4**.

Serial Port B

This setting allows you to decide how Serial Port B will be controlled. The options are **Enabled** (user defined), Disabled, Auto (BIOS controlled) and OS Controlled.

Mode

This setting allows you to set the type of a device that will be connected to Serial Port B. The options are **Normal** and IR (for an infrared device).

Base I/O Address

This setting allows you to select the base I/O address for Serial Port B. The options are 3F8, **2F8**, 3E8 and 2E8.

Interrupt

This setting allows you to select the IRQ (interrupt request) for Serial Port B. The options are **IRQ3** and IRQ4.

Parallel Port

This setting allows you to decide how the parallel port will be controlled. The options are **Enabled** (user defined), Disabled and Auto (BIOS-or OS- controlled).

Base I/O Address

Select the base I/O address for the parallel port. The options are **378**, 278 and 3BC.

Interrupt

This setting allows you to select the IRQ (interrupt request) for the parallel port. The options are IRQ5 and **IRQ7**.

Mode

This feature allows you to specify the parallel port mode. The options are Output only, Bi-Directional, EPP and **ECP**.

DMA Channel

This item allows you to specify the DMA channel for the parallel port. The options are DMA1 and **DMA3**.

Floppy Disk Controller

This setting allows you to decide how the floppy disk controller will be controlled. The options are **Enabled** (user defined), Disabled, and Auto (BIOS and OS controlled).

▶DMI Event Logging

Access the submenu to make changes to the following settings.

Event Log Validity

This is a display to inform you of the event log validity. It is not a setting.

Event Log Capacity

This is a display to inform you of the event log capacity. It is not a setting.

View DMI Event Log

Highlight this item and press <Enter> to view the contents of the event log.

Event Logging

This setting allows you to **Enable** or Disable event logging.

ECC Event Logging

This setting allows you to **Enable** or Disable ECC event logging.

Mark DMI Events as Read

Highlight this item and press <Enter> to mark the DMI events as read.

Clear All DMI Event Logs

Select Yes and press <Enter> to clear all DMI event logs. The options are Yes and **No**.

▶Console Redirection

Access the submenu to make changes to the following settings.

COM Port Address

This item allows you to specify which COM port to direct the remote console to: Onboard COM A or Onboard COM B. This setting can also be **Disabled**.

BAUD Rate

This item allows you to set the BAUD rate for console redirection. The options are 300, 1200, 2400, 9600, **19.2K**, 38.4K, 57.6K, and 115.2K.

Console Type

This item allows you to set console redirection type. The options are VT100, VT100/8bit, PC-ANSI/7bit, **PC ANSI**, VT100+, VT-UTF8 and ASCII.

Flow Control

This item allows you to select the flow control option for the console. The options are: None, XON/XOFF, and **CTS/RTS**.

Console Connection

This item allows you to decide how console redirection is to be connected: either **Direct** or Via Modem.

Continue CR after POST

Select on to continue with console redirection after the POST routine. The options are On and **Off**.

▶Hardware Monitoring

Highlight an item and hit <Enter> to see the status of the item:

CPU Temperature/System Temperature

Fan1 - Fan 6

If the feature of Auto Fan Control is enabled, the BIOS will automatically display the status of the fan as specified.

Fan Speed Control Modes

This feature allows the user to decide how the system controls the speeds of the onboard fans. The CPU temperature and the fan speed are correlative. When the CPU on-die temperature increases, the fan speed will also increase, and vice versa. Select Workstation if your system is used as a Workstation. Select Server if your system is used as a Server. Select 3-pin if your chassis uses 3-pin fans. Select 4-pin if your chassis uses 4-pin fans. Select "Disable" to disable the fan speed control function and allow the onboard fans to constantly run at the full speed (12V). The Options are: 1. **Disabled (Full Speed @12V),** 2. Optimized Server w/3-pin, 3. Optimized Workstation w/3-pin, 4. Optimized Server w/4-pin, and 5. Optimized Workstation w/4-pin.

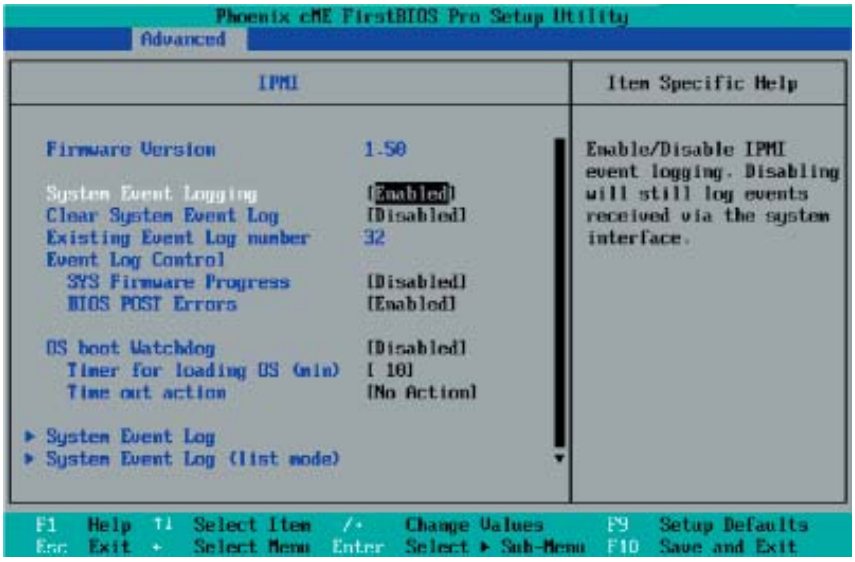
Voltage Monitoring

The following items will be monitored and displayed:

VcoreA,
Vcc 1.5
-12V/+12V
V_DIMM
+3.3V
5Vsb/5VDD
VTT_FSB
Vbatt.

Note: In the Windows OS environment, the Supero Doctor III settings take precedence over the BIOS settings. When first installed, Supero Doctor III adopts the temperature threshold settings previously set in the BIOS. Any subsequent changes to these thresholds must be made within Supero Doctor, since the SD III settings override the BIOS settings. For the Windows OS to adopt the BIOS temperature threshold settings, please change the SDIII settings to be the same as those set in the BIOS.

▶IPMI (The option is available only when an IPMI card is installed in the system.)


Phoenix cME FirstBIOS Pro Setup Utility
Advanced
IPMI
Item Specific Help
Firmware Version 1.50
System Event Logging [Enabled]
Clear System Event Log [Disabled]
Existing Event Log number 32
Event Log Control
SYS Firmware Progress [Disabled]
BIOS POST Errors [Enabled]
OS boot Watchdog [Disabled]
Timer for loading OS (min) [10]
Time out action [No Action]
▶ System Event Log
▶ System Event Log (list mode)
Enable/Disable IPMI event logging. Disabling will still log events received via the system interface.
F1 Help ↑↓ Select Item -/+ Change Values F9 Setup Defaults
Esc Exit ↔ Select Menu Enter Select ▶ Sub-Menu F10 Save and Exit

Firmware Version

This item displays the current Firmware Version.

System Event Logging

Select Enabled to enable IPMI Event Logging. When this function is set to Disabled, the system will continue to log events received via system interface. The options are **Enabled** and Disabled.

Clear System Event Logging

Enable this function to force the BIOS to clear the system event logs during the next cold boot. The options are Enabled and **Disabled**.

Existing Event Log Number

This item displays the number of the existing event log.

Event Log Control

System Firmware Progress

Enable this function to log POST progress. The options are Enabled and **Disabled**.

BIOS POST Errors

Enable this function to log POST errors. The options are Enabled and **Disabled**.

BIOS POST Errors

Enable this function to log POST errors. The options are Enabled and **Disabled**.

OS Boot Watch Dog

Set to Enabled to enable OS Boot Watch Dog. The options are Enabled and **Disabled**.

Timer for Loading OS (Minutes)

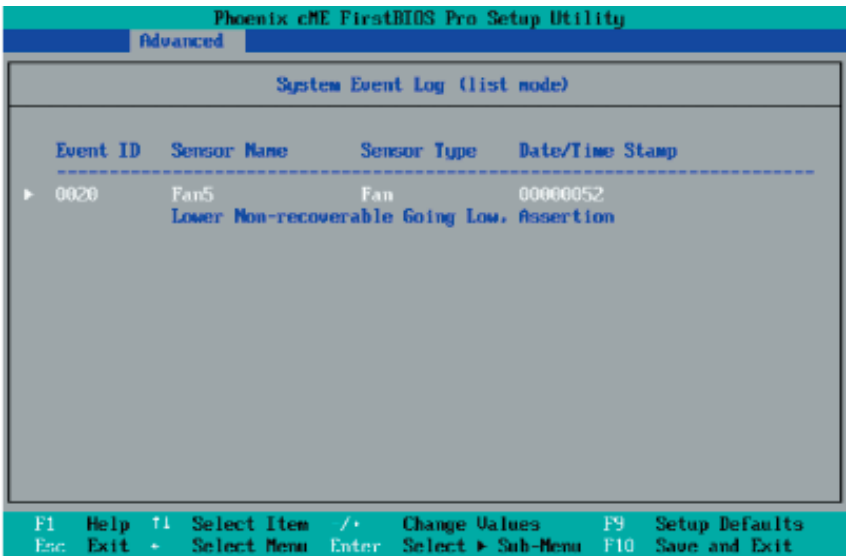
This feature allows the user to set the time value (in minutes) for the item: OS Boot Watch Dog by keying-in a desired number in the blank. The default setting is **10** (minutes.) (Please ignore this option when OS Boot Watch Dog is set to Disabled.)

Time Out Option

This item allows the user to decide what action the system should take in an event of a system boot failure. The options are **No Action**, Reset, Power Off and Power Cycles

▶System Event Log/System Event Log (List Mode)

These items display the System Event (SEL) Log and System Event (SEL) Log in the List Mode. Items include: SEL (System Event Log) Entry Number, SEL Record ID, SEL Record Type, Time Stamp, Generator ID, SEL Message Revision, Sensor Type, Sensor Number, SEL Event Type, Event Description, and SEL Event Data.



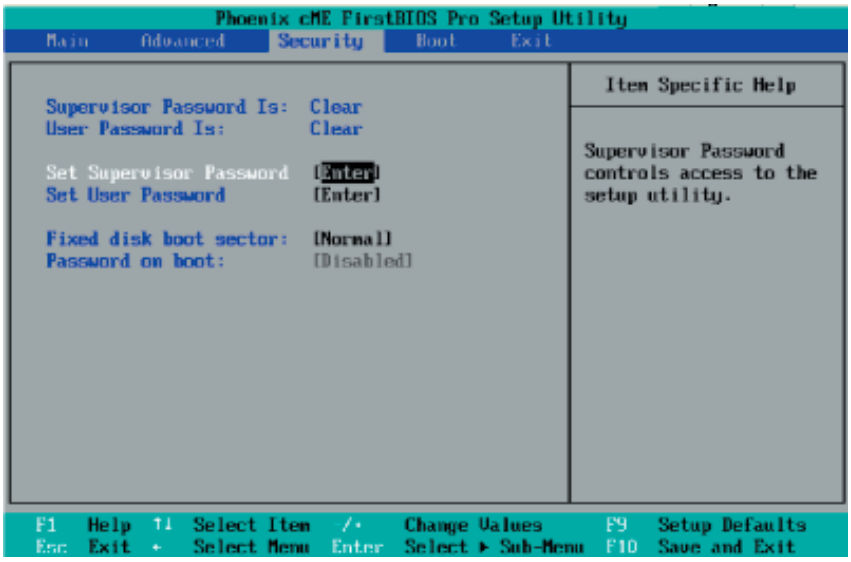
▶Realtime Sensor Data

This feature display information from motherboard sensors, such as temperatures, fan speeds and voltages of various components.

Phoenix cME FirstBIOS Pro Setup Utility
Advanced
System Event Log
SEL Entry Number = 1
SEL Record ID = 0001
SEL Record Type = 02 - System Event Record
Timestamp = 00000076
Generator Id = 40 00
SEL Message Rev = 04
Sensor Type = 15 - Module / Board
Sensor Number = 11 - CPU Internal Er
SEL Event Type = 03 - digital Discrete
Event Description = State Asserted, Assertion
SEL Event Data = 01 00 00
F1 Help ↑↓ Select Item -/+ Change Values F9 Setup Defaults
Esc Exit ← Select Menu Enter Select ▸ Sub-Menu F10 Save and Exit

4-5 Security Settings

Choose the Security menu from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. Security setting options are displayed by highlighting the setting using the arrow keys and pressing <Enter>. All Security BIOS settings are described in this section.


Phoenix cME FirstBIOS Pro Setup Utility
Main
Advanced
Security
Boot
Exit
Item Specific Help
Supervisor Password Is: Clear
User Password Is: Clear
Set Supervisor Password [Enter]
Set User Password [Enter]
Fixed disk boot sector: [Normal]
Password on boot: [Disabled]
Supervisor Password controls access to the setup utility.
F1 Help ↑↓ Select Item -/+ Change Values F9 Setup Defaults
Esc Exit ← Select Menu Enter Select ▸ Sub-Menu F10 Save and Exit

Supervisor Password Is:

This item indicates if a supervisor password has been entered for the system. Clear means such a password has not been used and Set means a supervisor password has been entered for the system.

User Password Is:

This item indicates if a user password has been entered for the system. Clear means such a password has not been used and Set means a user password has been entered for the system.

Set Supervisor Password

When the item Set Supervisor Password is highlighted, hit the <Enter> key. When prompted, type the Supervisor's password in the dialogue box to set or to change supervisor's password, which allows access to the BIOS.

Set User Password

When the item Set User Password is highlighted, hit the <Enter> key. When prompted, type the user's password in the dialogue box to set or to change the user's password, which allows access to the system at boot-up.

Fixed Disk Boot Sector

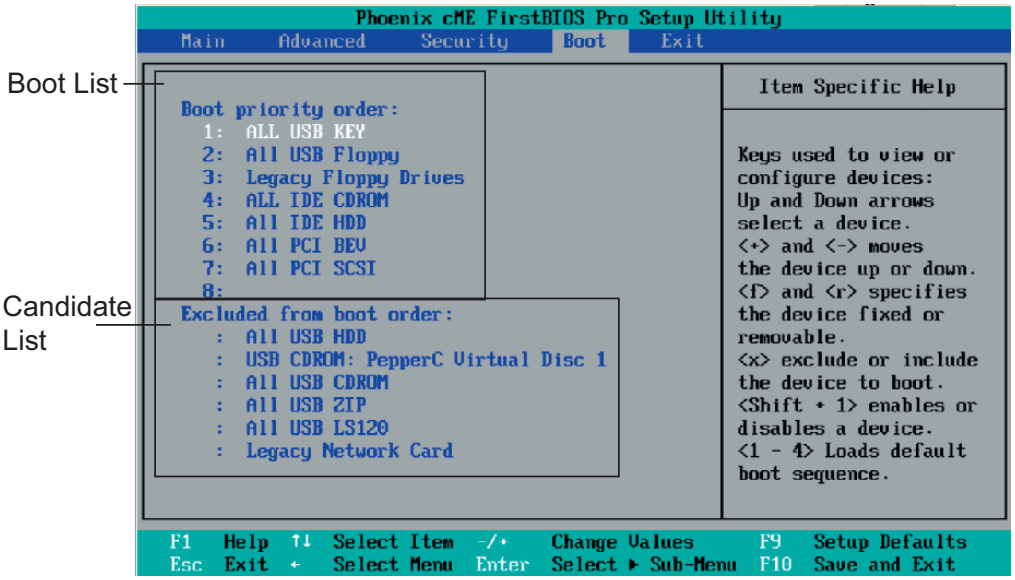
Select **Normal** to enable Write-Protect the boot sector on the hard drives to protect against viruses.

Password on Boot

This setting allows you to require a password to be entered when the system boots up. The options are Enabled (password required) and Disabled (password not required).

4-6 Boot Settings

Choose Boot from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. See details on how to change the order and specs of boot devices in the Item Specific Help window. All Boot BIOS settings are described in this section.

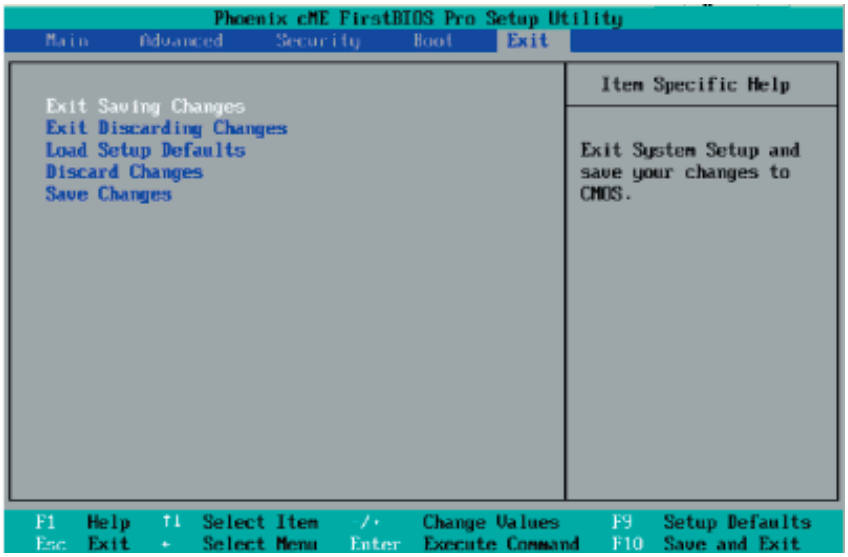

Boot List
Candidate List
Phoenix cME FirstBIOS Pro Setup Utility
Main
Advanced
Security
Boot
Exit
Boot priority order:
1: ALL USB KEY
2: All USB Floppy
3: Legacy Floppy Drives
4: ALL IDE CDROM
5: All IDE HDD
6: All PCI BEV
7: All PCI SCSI
8:
Excluded from boot order:
: All USB HDD
: USB CDROM: PepperC Virtual Disc 1
: All USB CDROM
: All USB ZIP
: All USB LS120
: Legacy Network Card
Item Specific Help
Keys used to view or configure devices:
Up and Down arrows select a device.
<+> and <-> moves the device up or down.
<f> and <r> specifies the device fixed or removable.
<x> exclude or include the device to boot.
<Shift + 1> enables or disables a device.
<1 - 4> Loads default boot sequence.
F1 Help ↑↓ Select Item -/+ Change Values F9 Setup Defaults
Esc Exit ← Select Menu Enter Select ▸ Sub-Menu F10 Save and Exit

Boot Priority Order/Excluded from Boot Orders

The devices included in the boot list section (above) are bootable devices listed in the sequence of boot order as specified. The boot functions for the devices included in the candidate list (above) are currently disabled. Use a <+> key or a <-> key to move the device up or down. Use the <f> key or the <r> key to specify the type of an USB device, either fixed or removable. You can select one item from the boot list and hit the <x> key to remove it from the list of bootable devices (to make its resource available for other bootable devices). Subsequently, you can select an item from the candidate list and hit the <x> key to remove it from the candidate list and put it in the boot list. This item will then become a bootable device. See details on how to change the priority of boot order of devices in the Item Specific Help window.

4-7 Exit

Choose Exit from the Phoenix BIOS Setup Utility main menu with the arrow keys. You should see the following display. All Exit BIOS settings are described in this section.



Exit Saving Changes

Highlight this item and hit <Enter> to save any changes you may have made and to exit the BIOS Setup utility.

Exit Discarding Changes

Highlight this item and hit <Enter> to exit the BIOS Setup utility without saving any changes you may have made.

Load Setup Defaults

Highlight this item and hit <Enter> to load the default settings for all items in the BIOS Setup. These are the safest settings to use.

Discard Changes

Highlight this item and hit <Enter> to discard (cancel) any changes you have made. You will remain in the Setup utility.

Save Changes

Highlight this item and hit <Enter> to save any changes you have made. You will remain in the Setup utility.

Notes

Appendix A

BIOS POST Messages

During the Power-On Self-Test (POST), the BIOS will check for problems. If a problem is found, the BIOS will activate an alarm or display a message. The following is a list of such BIOS messages.

Failure Fixed Disk

Fixed disk is not working or not configured properly. Check to see if fixed disk is attached properly. Run Setup. Find out if the fixed-disk type is correctly identified.

Stuck key

Stuck key on keyboard.

Keyboard error

Keyboard not working.

Keyboard Controller Failed

Keyboard controller failed test. May require replacing keyboard controller.

Keyboard locked - Unlock key switch

Unlock the system to proceed.

Monitor type does not match CMOS - Run SETUP

Monitor type not correctly identified in Setup

Shadow Ram Failed at offset: nnnn

Shadow RAM failed at offset **nnnn** of the 64k block at which the error was detected.

System RAM Failed at offset: nnnn

System RAM failed at offset **nnnn** of in the 64k block at which the error was detected.

Extended RAM Failed at offset: nnnn Extended memory not working or not configured properly at offset **nnnn.**

System battery is dead - Replace and run SETUP

The CMOS clock battery indicator shows the battery is dead. Replace the battery and run Setup to reconfigure the system.

System CMOS checksum bad - Default configuration used

System CMOS has been corrupted or modified incorrectly, perhaps by an application program that changes data stored in CMOS. The BIOS installed Default Setup Values. If you do not want these values, enter Setup and enter your own values. If the error persists, check the system battery or contact your dealer.

System timer error

The timer test failed. Requires repair of system board.

Real time clock error
Real-Time Clock fails BIOS hardware test. May require board repair.

Check date and time settings

BIOS found date or time out of range and reset the Real-Time Clock. May require setting legal date (1991-2099).

Previous boot incomplete - Default configuration used

Previous POST did not complete successfully. POST loads default values and offers to run Setup. If the failure was caused by incorrect values and they are not corrected, the next boot will likely fail. On systems with control of **wait states**, improper Setup settings can also terminate POST and cause this error on the next boot. Run Setup and verify that the waitstate configuration is correct. This error is cleared the next time the system is booted.

Memory Size found by POST differed from CMOS

Memory size found by POST differed from CMOS.

Diskette drive A error

Diskette drive B error

Drive A: or B: is present but fails the BIOS POST diskette tests. Check to see that the drive is defined with the proper diskette type in Setup and that the diskette drive is attached correctly.

Incorrect Drive A type - run SETUP

Type of floppy drive A: not correctly identified in Setup.

Incorrect Drive B type - run SETUP

Type of floppy drive B: not correctly identified in Setup.

System cache error - Cache disabled

RAM cache failed and BIOS disabled the cache. On older boards, check the cache jumpers. You may have to replace the cache. See your dealer. A disabled cache slows system performance considerably.

CPU ID:

CPU socket number for Multi-Processor error.

EISA CMOS not writeable

ServerBIOS2 test error: Cannot write to EISA CMOS.

DMA Test Failed

ServerBIOS2 test error: Cannot write to extended **DMA** (Direct Memory Access) registers.

Software NMI Failed

ServerBIOS2 test error: Cannot generate software NMI (Non-Maskable Interrupt).

Fail-Safe Timer NMI Failed

ServerBIOS2 test error: Fail-Safe Timer takes too long.

Device Address Conflict

Address conflict for specified **device.**

Allocation Error for: device

Run ISA or EISA Configuration Utility to resolve resource conflict for the specified **device**.

CD ROM Drive

CD ROM Drive identified.

Entering SETUP ...

Starting Setup program

Failing Bits: nnnn

The hex number **nnnn** is a map of the bits at the RAM address which failed the memory test. Each 1 (one) in the map indicates a failed bit. See errors 230, 231, or 232 above for offset address of the failure in System, Extended, or Shadow memory.

Fixed Disk n

Fixed disk **n** (0-3) identified.

Invalid System Configuration Data

Problem with NVRAM (CMOS) data.

I/O device IRQ conflict

I/O device IRQ conflict error.

PS/2 Mouse Boot Summary Screen:

PS/2 Mouse installed.

nnnn kB Extended RAM Passed

Where **nnnn** is the amount of RAM in kilobytes successfully tested.

nnnn Cache SRAM Passed

Where **nnnn** is the amount of system cache in kilobytes successfully tested.

nnnn kB Shadow RAM Passed

Where **nnnn** is the amount of shadow RAM in kilobytes successfully

tested.

nnnn kB System RAM Passed

Where **nnnn** is the amount of system RAM in kilobytes successfully tested.

One or more I2O Block Storage Devices were excluded from the Setup Boot Menu

There was not enough room in the IPL table to display all installed I2O block-storage devices.

Operating system not found

Operating system cannot be located on either drive A: or drive C:. Enter Setup and see if fixed disk and drive A: are properly identified.

Parity Check 1 nnnn

Parity error found in the system bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays **????.** Parity is a method for checking errors in binary data. A parity error indicates that some data has been corrupted.

Parity Check 2 nnnn

Parity error found in the I/O bus. BIOS attempts to locate the address and display it on the screen. If it cannot locate the address, it displays **????.**

Press <F1> to resume, <F2> to Setup, <F3> for previous

Displayed after any recoverable error message. Press <F1> to start the boot process or <F2> to enter Setup and change the settings. Press <F3> to display the previous screen (usually an initialization error of an **Option ROM**, i.e., an add-on card). Write down and follow the information shown on the screen.

Press <F2> to enter Setup

Optional message displayed during POST. Can be turned off in Setup.

PS/2 Mouse:

PS/2 mouse identified.

Run the I2O Configuration Utility

One or more unclaimed block storage devices have the Configuration Request bit set in the LCT. Run an I2O Configuration Utility (e.g. the SAC utility).

System BIOS shadowed

System BIOS copied to shadow RAM.

UMB upper limit segment address: *nnnn*

Displays the address ***nnnn*** of the upper limit of **Upper Memory Blocks**, indicating released segments of the BIOS which can be reclaimed by a virtual memory manager.

Video BIOS shadowed

Video BIOS successfully copied to shadow RAM.

Appendix B

BIOS POST Codes

This section lists the POST (Power On Self Test) codes for the PhoenixBIOS. POST codes are divided into two categories: recoverable and terminal.

Recoverable POST Errors

When a recoverable type of error occurs during POST, the BIOS will display an POST code that describes the problem. BIOS may also issue one of the following beep codes:

1 long and two short beeps - video configuration error
1 repetitive long beep - no memory detected

Terminal POST Errors

If a terminal type of error occurs, BIOS will shut down the system. Before doing so, BIOS will write the error to port 80h, attempt to initialize video and write the error in the top left corner of the screen. The following is a list of codes that may be written to port 80h.

POST Code	Description
02h	Verify Real Mode
03h	Disable Non-Maskable Interrupt (NMI)
04h	Get CPU type
06h	Initialize system hardware
07h	Disable shadow and execute code from the ROM.
08h	Initialize chipset with initial POST values
09h	Set IN POST flag
0Ah	Initialize CPU registers
0Bh	Enable CPU cache
0Ch	Initialize caches to initial POST values
0Eh	Initialize I/O component
0Fh	Initialize the local bus IDE
10h	Initialize Power Management
11h	Load alternate registers with initial POST values
12h	Restore CPU control word during warm boot
13h	Reset PCI Bus Mastering devices
14h	Initialize keyboard controller
16h	1-2-2-3 BIOS ROM checksum
17h	Initialize cache before memory Auto size

POST Code	Description
18h	8254 timer initialization
1Ah	8237 DMA controller initialization
1Ch	Reset Programmable Interrupt Controller
20h	1-3-1-1 Test DRAM refresh
22h	1-3-1-3 Test 8742 Keyboard Controller
24h	Set ES segment register to 4 GB
28h	Auto size DRAM
29h	Initialize POST Memory Manager
2Ah	Clear 512 kB base RAM
2Ch	1-3-4-1 RAM failure on address line ***xxxx****
2Eh	1-3-4-3 RAM failure on data bits ***xxxx**** of low byte of memory bus
2Fh	Enable cache before system BIOS shadow
32h	Test CPU bus-clock frequency
33h	Initialize Phoenix Dispatch Manager
36h	Warm start shut down
38h	Shadow system BIOS ROM
3Ah	Auto size cache
3Ch	Advanced configuration of chipset registers
3Dh	Load alternate registers with CMOS values
41h	Initialize extended memory for RomPilot (optional)
42h	Initialize interrupt vectors
45h	POST device initialization
46h	2-1-2-3 Check ROM copyright notice
48h	Check video configuration against CMOS
49h	Initialize PCI bus and devices
4Ah	Initialize all video adapters in system
4Bh	QuietBoot start (optional)
4Ch	Shadow video BIOS ROM
4Eh	Display BIOS copyright notice
4Fh	Initialize MultiBoot
50h	Display CPU type and speed
51h	Initialize EISA board (optional)
52h	Test keyboard
54h	Set key click if enabled
55h	Enable USB devices
58h	2-2-3-1 Test for unexpected interrupts
59h	Initialize POST display service
5Ah	Display prompt “Press <ESC> to enter SETUP”
5Bh	Disable CPU cache

POST Code	Description
5Ch	Test RAM between 512 and 640 kB
60h	Test extended memory
62h	Test extended memory address lines
64h	Jump to UserPatch1
66h	Configure advanced cache registers
67h	Initialize Multi Processor APIC
68h	Enable external and CPU caches
69h	Setup System Management Mode (SMM) area
6Ah	Display external L2 cache size
6Bh	Load custom defaults (optional)
6Ch	Display shadow-area message
70h	Display error messages
72h	Check for configuration errors
76h	Check for keyboard errors
7Ch	Set up hardware interrupt vectors
7Dh	Initialize Intelligent System Monitoring (optional)
7Eh	Initialize coprocessor if present
80h	Disable onboard Super I/O ports and IRQs (optional)
81h	Late POST device initialization
82h	Detect and install external RS232 ports
83h	Configure non-MCD IDE controllers
84h	Detect and install external parallel ports
85h	Initialize PC-compatible PnP ISA devices
86h	Re-initialize onboard I/O ports.
87h	Configure Motherboard Configurable Devices (optional)
88h	Initialize BIOS Data Area
89h	Enable Non-Maskable Interrupts (NMIs)
8Ah	Initialize Extended BIOS Data Area
8Bh	Test and initialize PS/2 mouse
8Ch	Initialize floppy controller
8Fh	Determine number of ATA drives (optional)
90h	Initialize hard-disk controllers
91h	Initialize local-bus hard-disk controllers
92h	Jump to UserPatch2
93h	Build MPTABLE for multi-processor boards
95h	Install CD ROM for boot
96h	Clear huge ES segment register
97h	Fix up Multi Processor table
98h	1-2 Search for option ROMs and shadow if successful. One long, two short beeps on checksum failure

POST Code	Description
99h	Check for SMART Drive (optional)
9Ch	Set up Power Management
9Dh	Initialize security engine (optional)
9Eh	Enable hardware interrupts
9Fh	Determine number of ATA and SCSI drives
A0h	Set time of day
A2h	Check key lock
A4h	Initialize typematic rate
A8h	Erase <ESC> prompt
AAh	Scan for <ESC> key stroke
ACh	Enter SETUP
AEh	Clear Boot flag
B0h	Check for errors
B1h	Inform RomPilot about the end of POST (optional)
B2h	POST done - prepare to boot operating system
B4h	1 One short beep before boot
B5h	Terminate QuietBoot (optional)
B6h	Check password (optional)
B7h	Initialize ACPI BIOS and PPM Structures
B9h	Prepare Boot
BAh	Initialize SMBIOS
BCh	Clear parity checkers
BDh	Display MultiBoot menu
BEh	Clear screen (optional)
BFh	Check virus and backup reminders
C0h	Try to boot with INT 19
C1h	Initialize POST Error Manager (PEM)
C2h	Initialize error logging
C3h	Initialize error display function
C4h	Initialize system error flags
C6h	Console redirection init.
C7h	Unhook INT 10h if console redirection enabled
C8h	Force check (optional)
C9h	Extended ROM checksum (optional)
CDh	Reclaim console redirection vector

POST Code	Description
D2h	Unknown interrupt
D4h	Check Intel Branding string
D8h	Alert Standard Format initialization
DEh	Log error if micro-code not updated properly

The following are for boot block in Flash ROM

POST Code	Description
E0h	Initialize the chipset
E1h	Initialize the bridge
E2h	Initialize the CPU
E3h	Initialize system timer
E4h	Initialize system I/O
E5h	Check force recovery boot
E6h	Checksum BIOS ROM
E7h	Go to BIOS
E8h	Set Huge Segment
E9h	Initialize Multi Processor
EAh	Initialize OEM special code
EBh	Initialize PIC and DMA
ECh	Initialize Memory type
EDh	Initialize Memory size
EEh	Shadow Boot Block
EFh	System memory test
F0h	Initialize interrupt vectors
F1h	Initialize Run Time Clock
F2h	Initialize video
F3h	Initialize System Management Manager
F4h	Output one beep
F5h	Clear Huge Segment
F6h	Boot to Mini DOS
F7h	Boot to Full DOS

* If the BIOS detects errors on 2C, 2E, or 30 (base 512K RAM error), it displays an additional word-bitmap (***xxxx***) indicating the address line or bits that have failed. For example, “2C 0002” means address line 1 (bit one set) has failed. “2E 1020" means data bits 12 and 5 (bits 12 and 5 set) have failed in the lower 16 bits. The BIOS also sends the bitmap to the port-80 LED display. It first displays the checkpoint code, followed by a delay, the high-order byte, another delay, and then the low-order byte of the error. It repeats this sequence continuously.

Notes

Appendix C

Intel HostRAID Setup Guidelines

After all the hardware has been installed, you must first configure Intel South Bridge RAID settings before you install the OS and other software drivers. To configure RAID settings, please refer to RAID Configuration User Guides posted on our web site at www.supermicro.com/support/manuals.

C-1 Installing the Windows XP/2000/2003 for systems with RAID Functions

1. Insert Microsoft's Windows XP/2000/2003 Setup CD in the CD Driver, and the system will start booting up from CD.

2. Press the <F6> key when the message-" Press F6 if you need to install a third party SCSI or RAID driver" displays.

3. When the Windows XP/2000/2003 Setup screen appears, press "S" to specify additional device(s).

4. Insert the driver diskette-"ITE RAID XP/2000/2003 Driver for IDE" into Drive A: and press the <Enter> key.

5. When the Windows XP/2000/2003 Setup screen appears, press "S" to specify additional device(s).

6. Insert the driver diskette-"Intel AA RAID XP/2000/2003 Driver for ICH9R into Drive A: and press the <Enter> key.

7. Select the Intel(R)82801GR/GH *SATA RAID Controller* from the list indicated in the XP/2000/2003 Setup Screen, and press the <Enter> key.

8. Press the <Enter> key to continue the installation process. (If you need to specify any additional devices to be installed, do it at this time.) Once all devices are specified, press the <Enter> key to continue with the installation.

9. From the Windows XP/2000/2003 Setup screen, press the <Enter> key. The XP/2000/2003 Setup will automatically load all device files and then, continue the Windows XP/2000/2003 installation.

10. After the Windows XP/2000/2003 Installation is completed, the system will automatically reboot.

11. Insert the Supermicro Setup CD that came with the package into the CD Drive during system reboot, and the main screen will appear.

C-2 Installing the Windows XP/2000/2003 for systems without RAID Functions

1. Insert Microsoft's Windows XP/2000/2003 Setup CD in the CD Driver, and the system will start booting up from CD.

2. Press the <F6> key when the message-" Press F6 if you need to install a third party SCSI or RAID driver" displays.

3. When the Windows XP/2000/2003 Setup screen appears, press "S" to specify additional device(s).

4. Insert the driver diskette-"ITE RAID XP/2000/2003 Driver for IDE" into Drive A: and press the <Enter> key.

5. Press the <Enter> key to continue the installation process. (If you need to specify any additional devices to be installed, do it at this time.) Once all devices are specified, press the <Enter> key to continue with the installation.

6. From the Windows XP/2000/2003 Setup screen, press the <Enter> key. The XP/2000/2003 Setup will automatically load all device files and then, continue the Windows XP/2000/2003 installation.

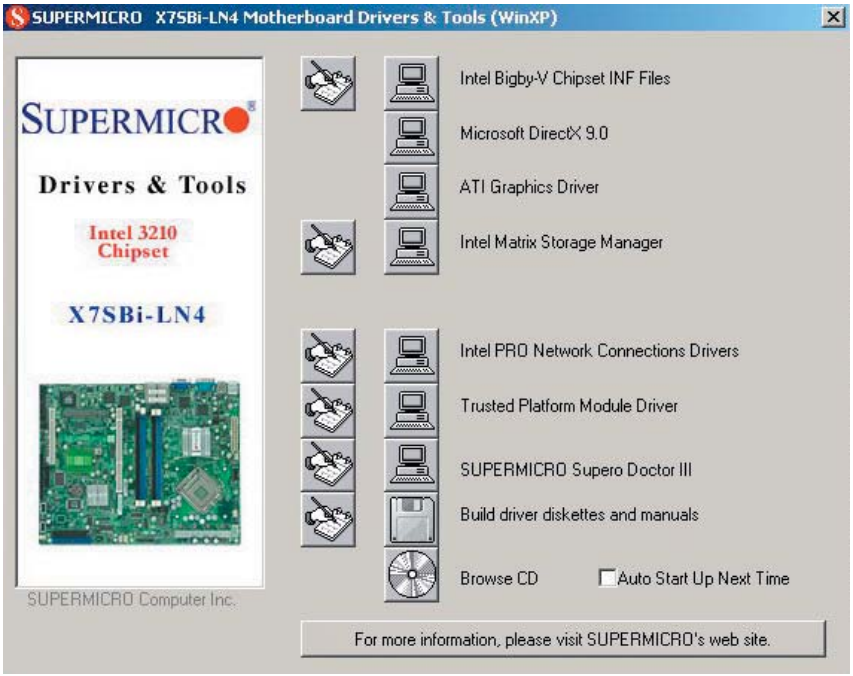
7. After the Windows XP/2000/2003 OS Installation is completed, the system will automatically reboot.

Appendix D

Installing Other Software Programs and Drivers

D-1 Installing Drivers other than the Adaptec Embedded Serial ATA RAID Controller Driver

After you've installed the Windows Operating System, a screen as shown below will appear. You are ready to install software programs and drivers that have not yet been installed. To install these software programs and drivers, click the icons to the right of these items.



Driver/Tool Installation Display Screen

***Note**: Click the icons showing a hand writing on the paper to view the readme files for each item. Click a computer icon to the right of an item to install an item (from top to the bottom) one at a time. After installing each item, you must re-boot the system before proceeding with the next item on the list. The bottom icon with a CD on it allows you to view the entire contents of the CD.*

D-2 Configuring Supero Doctor III

The Supero Doctor III program is a Web-base management tool that supports remote management capability. It includes Remote and Local Management tools. The local management is called the SD III Client. The Supero Doctor III program included on the CDROM that came with your motherboard allows you to monitor the environment and operations of your system. Supero Doctor III displays crucial system information such as CPU temperature, system voltages and fan status. See the Figure below for a display of the Supero Doctor III interface.

Note: *The default username and password are ADMIN.*

Note 2: *In the Windows OS environment, the Supero Doctor III settings take precedence over the BIOS settings. When first installed, Supero Doctor III adopts the temperature threshold settings previously set in the BIOS. Any subsequent changes to these thresholds must be made within Supero Doctor, since the SD III settings override the BIOS settings. For the Windows OS to adopt the BIOS temperature threshold settings, please change the SDIII settings to be the same as those set in the BIOS.*

Supero Doctor III Interface Display Screen-I (Health Information)

Supero Doctor III Interface Display Screen-II (Remote Control)

File Edit View Favorites Tools Help
Super Doctor III
Remote Management
SUPERMICRO
Remote Control
Graceful Power Control
Open Console
Power Control
Enter
Graceful power control
Supero Doctor III allows a user to inform the OS to reboot or shut down within a specified time (the default is 30 seconds). Before the system reboots or shuts down, it's allowed to cancel the action.
Requirements
Keep Supero SD3Service Daemon running at all times on this system.
Provide TCP/IP connectivity.
Power control

***Note**: SD III Software Revision 1.0 can be downloaded from our Web site at: ftp://ftp.supermicro.com/utility/Supero_Doctor_III/. You can also download SDIII User's Guide at: http://www.supermicro.com/PRODUCT/Manuals/SDIII/UserGuide.pdf. For Linux, we will still recommend that you use Supero Doctor II.*

Notes